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IPC Solution

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**Small Cube System**  
**Fanless Series**  
**FX5636 User' Manual**

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- ❑ A list of your name, address, telephone, facsimile number, or email address where you may be reached during the day
- ❑ Description of you peripheral attachments
- ❑ Description of your software (operating system, version, application software, etc.) and BIOS configuration
- ❑ Description of the symptoms (Extract wording any message)

For updated BIOS, drivers, manuals, or product information, please visit us at [www.fabiatech.com](http://www.fabiatech.com)

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## Chapter 1 Introducing the FX5636 System

### Overview

The FX5636 is an embedded system with Intel® Atom™ D525 low-power CPU module inside. This user's manual provides information on the physical features, installation, and BIOS setup of the FX5636.

Built to unleash the total potential of the Intel® Atom™ D525 -Dual Core Processor, Able to support 1.8 GHz CPU, this system supports two 10/100/1000M Base -TX LAN ports, one PCIe Mini Card connector for Wireless/GPRS modules, six USB2.0 ports, Audio, and two So-DIMM socket supports up to 4GB DDR3 RAM, one expansion PCI slot and a VGA controller.

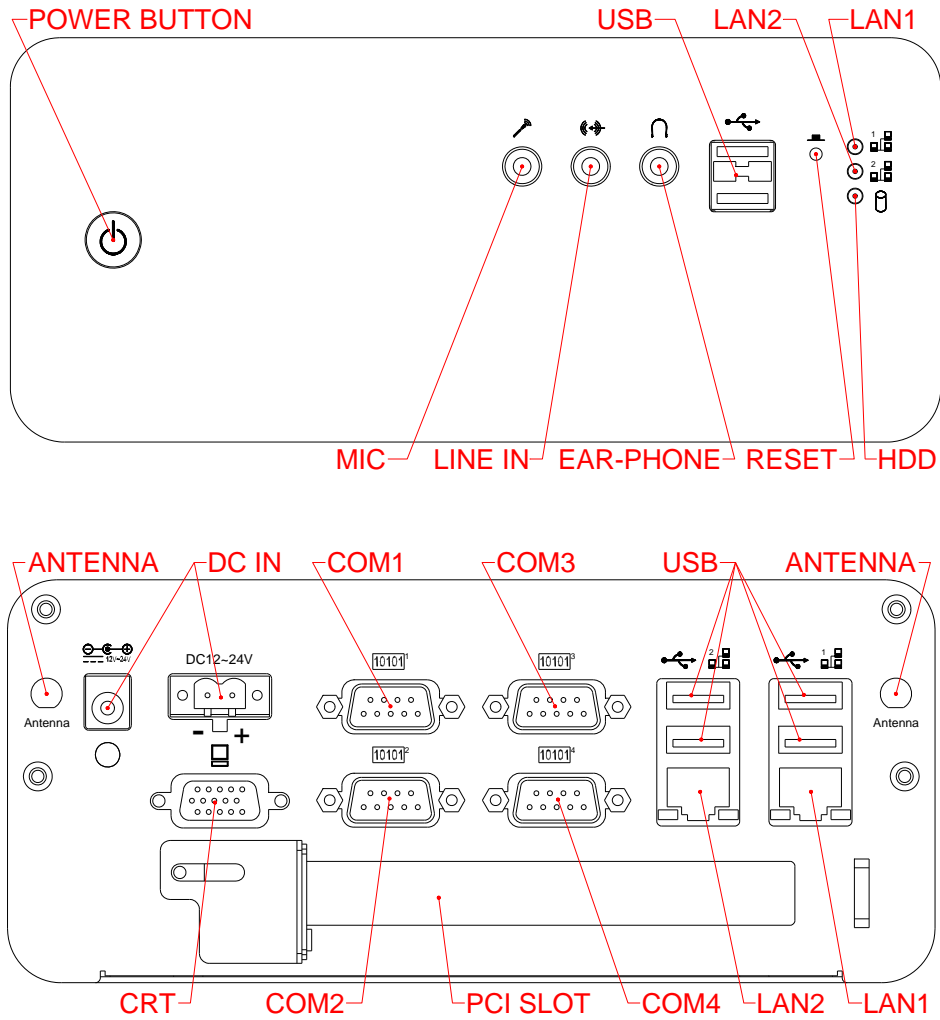
Each FX5636 has four ports for I/O communications. Three RS-232C ports and one RS232/RS422/RS485 port are available.

The FX5636 is perfect for ATM machines, KIOSK, point-of-sales/point-of- information, gaming and infotainment, measurement technology, lotteries, banking and Thin Client and small Embedded Control. The unit is only 190mm (W) X240mm (D) X82mm (H).

## Series Comparison Table

Model	FX5636
Processor	Intel® Atom™ D525 1.8GHz
<i>N+S-Chipset</i>	82801HBM(ICH8M)
<i>Memory 204 Pin-DIMM*2(Max.)</i>	DDR3/800 2GB/4GB
<i>Storage Space</i>	One CFAST CF and One SATA
<i>Display</i>	VGA/CRT
<i>Watchdog Timer</i>	Yes
<i>Multi I/O</i>	Three RS-232 and One RS232/RS422/RS485
<i>USB 2.0</i>	Six USB ports
<i>Audio</i>	Ear-Phone/Line-In And MIC-In
<i>RJ45 LAN port</i>	Two Realtek RTL8111C
<i>PCIe Mini Card</i>	One
<i>PCI Bus Expansion</i>	One/+12V& -12V
Operating Temperature	0~+50°C (32~122°F)
Storage Temperature	-20~+60°C (4~140°F)
Dimensions (Unit: mm)	190(W) x 240(D) X 82(H)

## Layout





## Specifications

### ❑ *Processor Board –*

Intel® Atom™ D525 Dual Core (1M L2 Cache, 1.8GHz) Low Power Processor with 2GB DDR3-RAM

### ❑ *I/O Outlets –*

Two 10/100/1000 base-TX Ethernet LAN ports

Six USB ports (2.0) and Three RS-232 and one RS232/422/485 ports with DB9

One VGA display connector with DB15

Three audio connectors for Earphone, Line-In and Microphone-in

One PCIe Mini card socket modules, especially for WLAN/GPRS module

One free PCI slot for add-on PCI card (PCI- 2.1 Specification)

One Push button reset switch

One DC-In jack connector/terminal block with power push button switches

### ❑ *LED Indicator –*

One power LED, One hard disk/CF access LED, and two LAN-Access LEDs

### ❑ *Storage Bay-*

CFAST Compact Flash socket for CFAST Compact Flash modules

One 2.5" SATA hard disk space

### ❑ *Power requirement –*

+12 ~ +24V DC, 2.88A maximum (1.47A typical) with 19V input voltage

### *Dimensions -*

190mm (W) x 240mm (D) x 82mm (H)

## Packing List

Upon receiving the package, verify the following things. Should any of the mentioned happens, contact us for immediate service.

- Unpack and inspect the FX5636 package for possible damage that may occur during the delivery process.
- Verify the accessories in the package according to the packing list and see if there is anything missing or incorrect package is included.
- If the cable(s) you use to install the FX5636 is not supplied from us, please make sure the specification of the cable(s) is compatible with the FX5636 system.

**Note:** after you install the FX5636, it is recommended that you keep the diskette or CD that contains drivers and document files, and keep the document copies, or unused cables in the carton for future use.

The following lists the accessories that may be included in your FX5636 package. Some accessories are optional items that are only shipped upon order.

- One FX5636x embedded system.
- One AC to DC power adapter and 1 AC power cord.
- One pack of 2.5" SATA hard disk installation kit with fixed screws.
- One 2-pin apartable terminal block.
- One compact disc includes software utility and manual.

Optional:

- AK1006- Half size mini PCIe module adapter kits. (PN: 0606010028G)
- FX5636K1- Wall mounting Kit (P/N:0606010027G)
- One DDR3 2GB/1333 204PIN SODIMM. (PN: 0909075201G-02)
- Dual-USB port interface cable (L: 300mm) with Bracket. (PN:7002020003G)
- One Serial and parallel port interface cable with bracket. (PN:7001000014G)
- Two serial ports interface cable with bracket.(PN:7001000093G)



## Chapter 2 Hardware Installation

This chapter introduces the system connectors & jumper settings, and guides you to apply them for field application.

### Before Installation

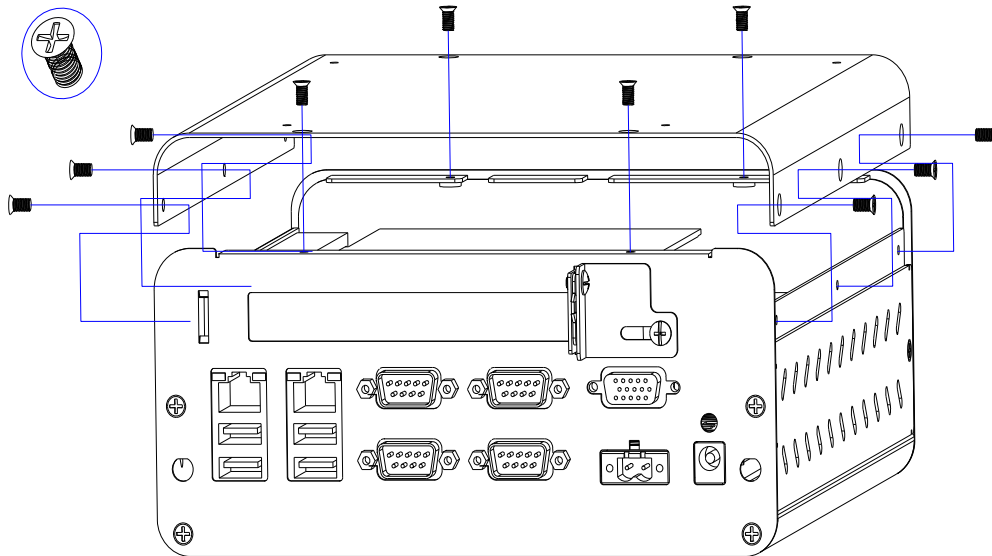
Before you install the system, make sure you follow the following descriptions.

1. Before removing the cover, shut down the operation System and disconnect power switch to off and unplug AC-to DC Adapter cable.
2. Install or unplug any connector, Compact Flash, and hard disk be sure that the power is disconnected or power switch to off from the system. If not, this may damage the system.
3. The ESD (Electricity Static Discharge) may be created from human body that touches the board. It may do damage to the board circuit.

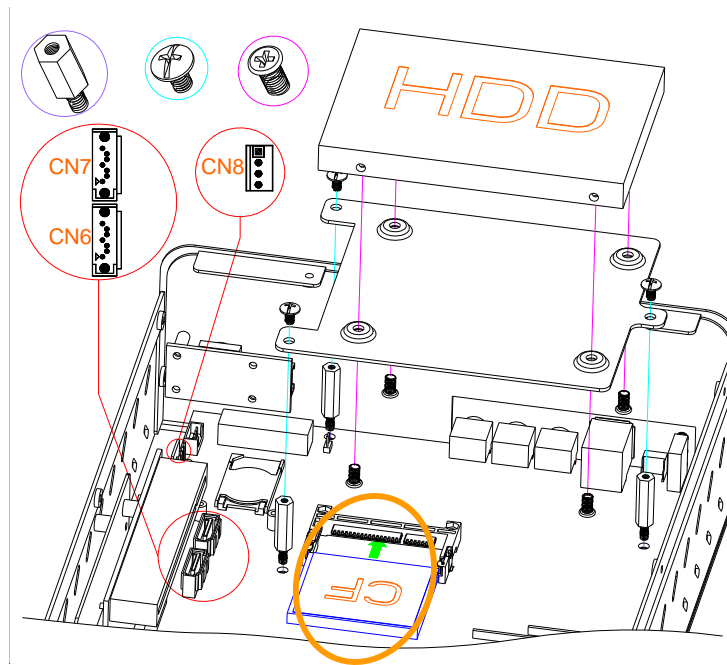
□ **Removing Covers -Installing Hardware**

If you are installing hardware option, you can remove the bottom cover. The following figure will guide you how to install SATA 2.5" HDD inside, C-Fast Compact Flash modules, mini PCIe WLAN or GPRS module, PCI card and DDR3-RAM module to the FX5636. (Please see the spots circled.)

**a. Unscrew Bottom cover**



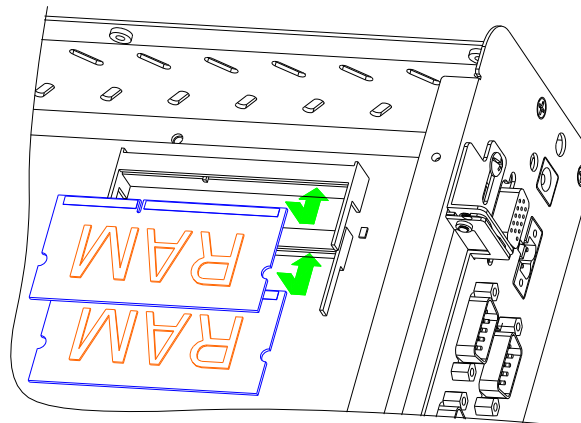
### b. Installing CFAST Compact Flash



**Note:** The CFAST Compact Flash socket supports CFAST Compact Flash Modules.

### c. Installing Memory

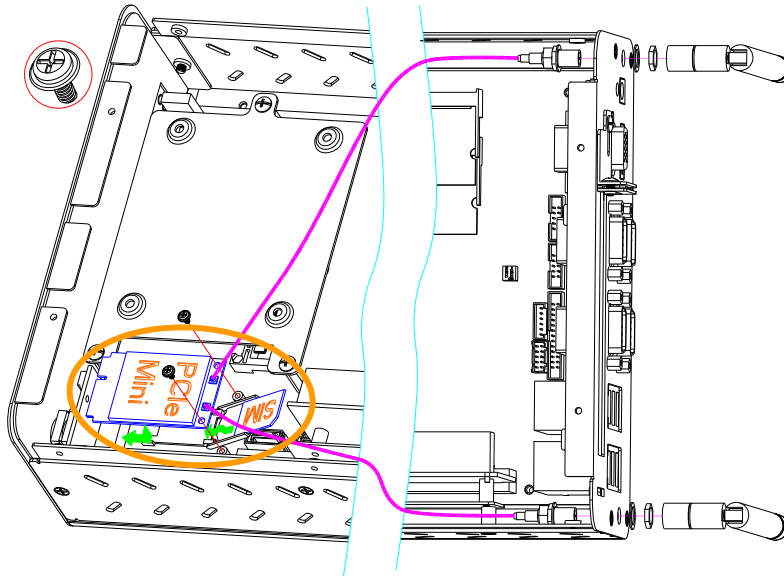
You may extend additional memory to FX5636, See as following figure. The 204 pin So-DIMM socket supports 1GB to 2GB (Max .4GB) of DDR3 RAM modules.



#### d. Installing PCIe Mini Card Module

You may extend additional PCIe mini card module and SIM card to FX5636. Connect the antenna cable from backside antenna hole to GPRS or Wireless LAN module and installing the SIM card for GPRS. See as following figure and rear pictures.

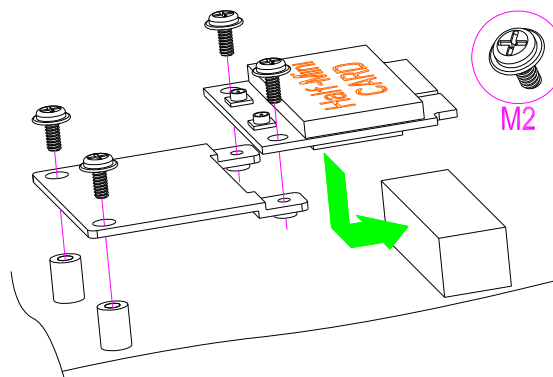
##### ❖ d1. Installing PCIe Mini Card and SIM card



**Note:**

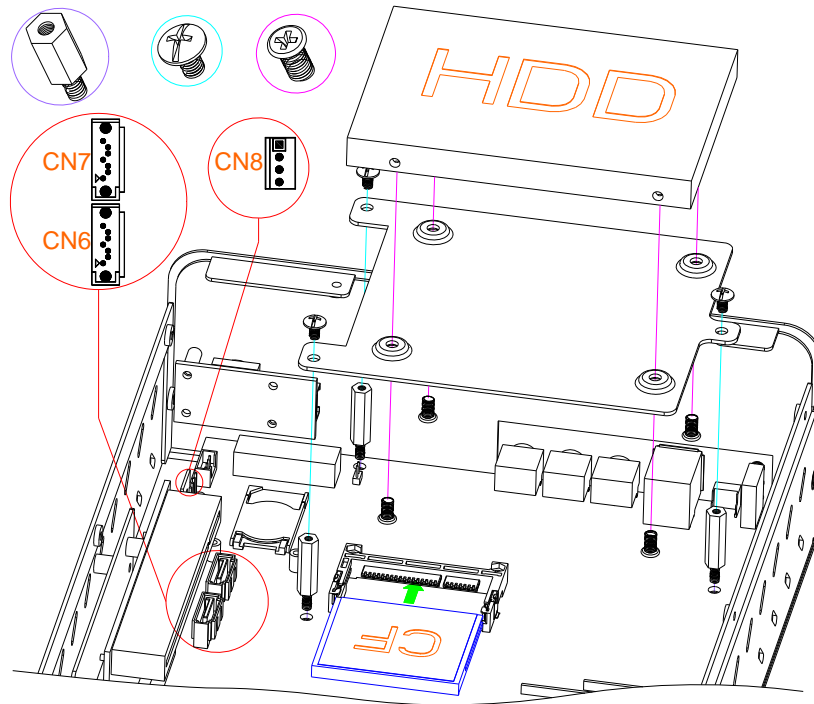
1. When installing PCIe GPRS Mini card on FX5636 system these is need the installing the SIM Card to system board.
2. Open the cover with SIM socket then insert SIM card into the SIM card hold. Make sure that the SIM card is properly inserted and that golden contact area on the card is facing downwards.

##### ❖ d2. Installing Half Size Mini PCIe module kit (Optional)



### e. Installing Hard Disk

Fasten screws up the Hard disk device to HDD metal frame and connect the SATA HDD cable and power cable. See as following figure and rear pictures.



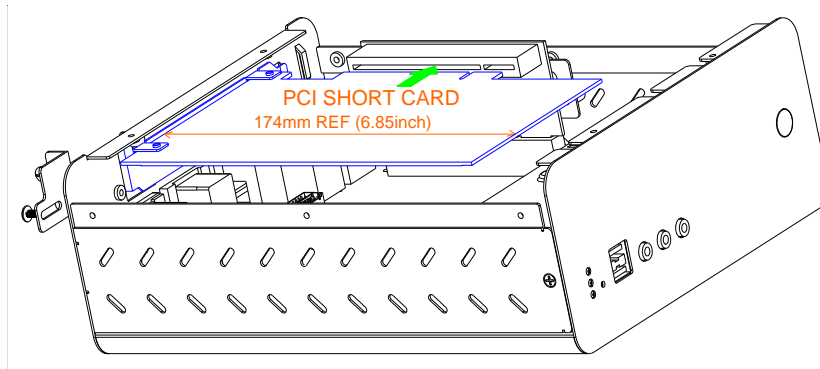
**Note:** Be careful with the pin orientation when installing connectors and the cables. CN6 and CN7 are used to connect a SATA 2.5" HDD with included SATA cable, CN8 is for SATA power connector. (The CN8 support +5V and +12V Voltage.)



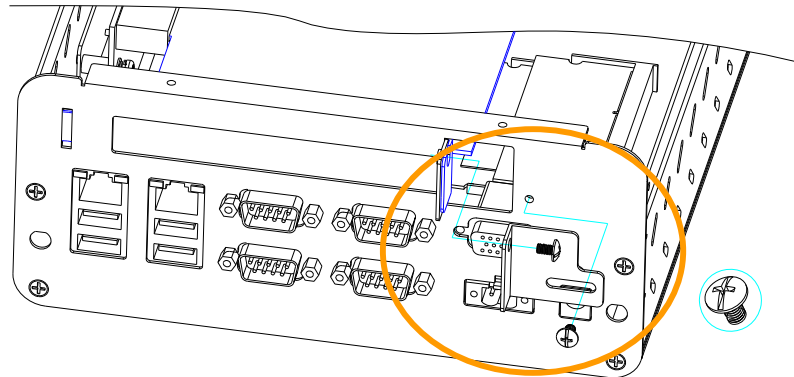
## f. Installing Expansion PCI Card

If you are installing Expansion PCI hardware, you can remove the top side cover. The following figure will guide you how to install PCI interface card inside the FX5636 (The PCI Slot supports +3.3V/+5V/+12V/-12V (500mA), PCI 2.1 Spec.).

### ❖ f1. Installing PCI card

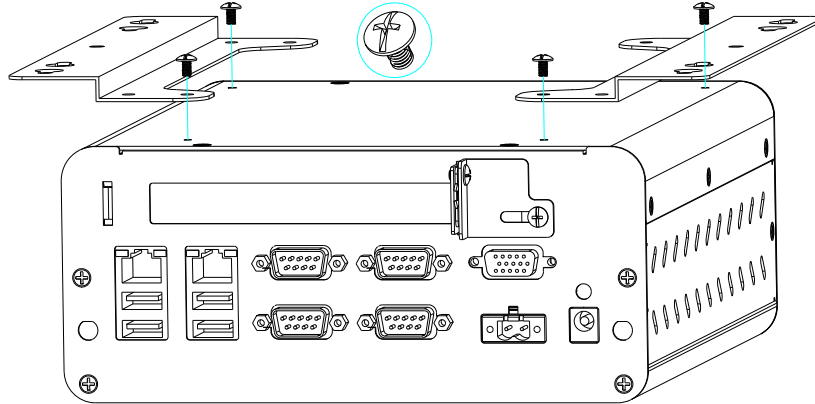


### ❖ f2. Screw the PCI card: Screw the L-Type Metal on the PCI card Bracket

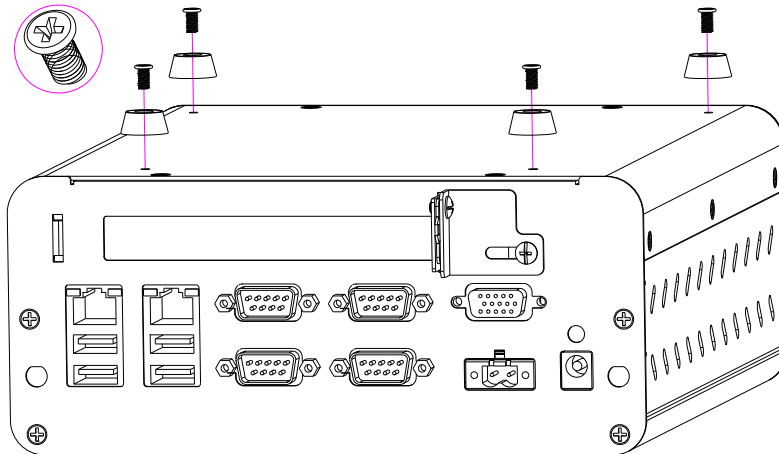


### g. Installing the FX5636K1 -Wall Mounting

Please refer to the down side figure for installing the FX5636 with universal fixers.

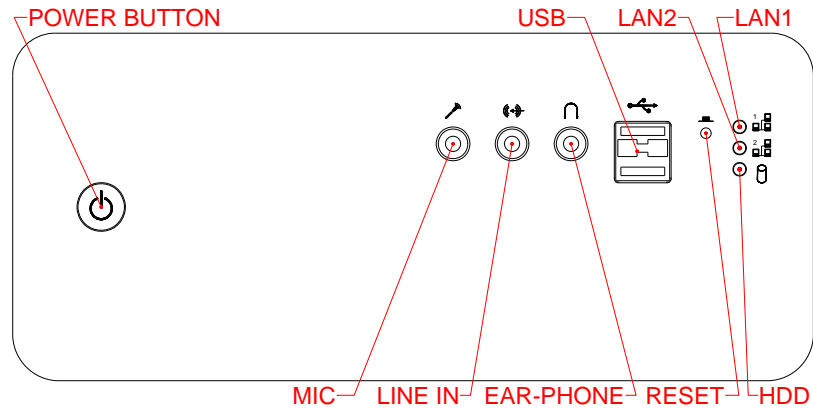


### h. Installing the Rubber Feet



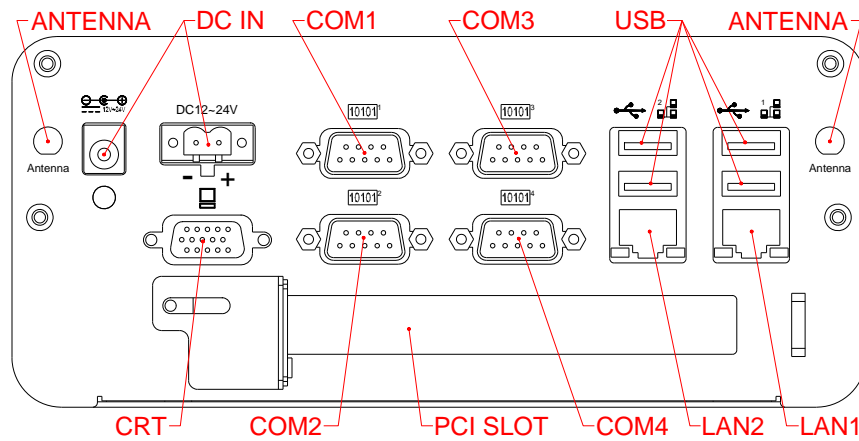
□ **LED Indicators (On the Front Panel)**

The Power Button and HDD LED's have two distinctive statuses: Off for inactive operation and blinking light for activity. And the 2 LED's for LAN ports. The LAN1 and LAN2 LED's (Green) indicate on-line/access status of LAN1 and LAN2 respectively.

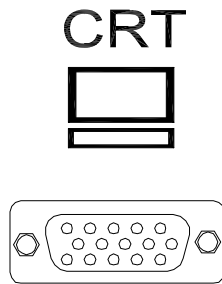


□ **I/O Peripheral Connectors**

View from the back side, If you are connecting the monitor, LAN, audio, COM and USB to the FX5636. See following figure and a side pictures.



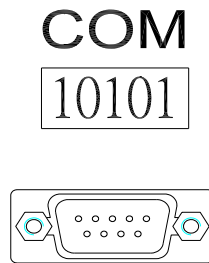
1. A VGA (CRT) connector is provided for VGA signals.



DB15	Signal
1	Red
2	Green
3	Blue
13	Hsync
14	Vsync
12	DDC Data
15	DDC Clock
5 & 10	Digital Ground
6,7,8	Analog Ground
Others	Not Used

2. COM ports

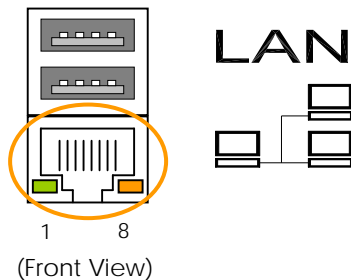
The DB9 (COM1/2/3/4) is standard serial port connector. The following tables show the signal connections of these connectors.



DB-9	RS-232 Signal
1	-DCD
6	-DSR
2	RXD
7	-RTS
3	-TXD
8	-CTS
4	-DTR
9	-RI
5	Ground
Case	Case Ground

3. LAN ports

The RJ45 connector with 2 LED's for WAN/LAN. The right side LED (orange) indicates data is being accessed and the left side LED (green) indicates on-line status. (On indicates on-line and off indicates off-line)

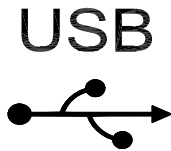
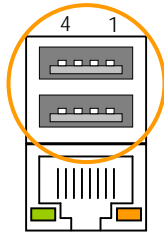


The following lists the pin assignment of RJ45.

LAN1/LAN2	Signal	LAN1/LAN2	Signal
1	TPTX+	5	TPTX1-
2	TPTX -	6	TPRX -
3	TPRX+	7	TPRX1+
4	TPTX1+	8	TPRX1-

#### 4. USB Ports

The FX5636 supports a six port USB connector. Any USB device can be attached to USB ports with plug-and-play supported. The front side port is USB #1/2, and back side is USB # 3/4 and USB # 5/6.



USB	Signal
Pin 1	USBV
Pin 2	USBD-
Pin 3	USBD+
Pin 4	USBG

(Front View)

#### 5. Connecting the Audio Mic In/ Line- In/ Ear-Phone Out



Mic-In

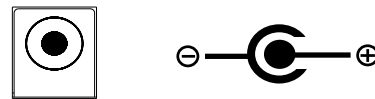
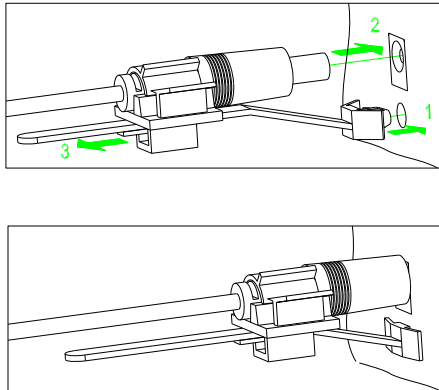
Line-In

EAR-Phone

□ **Connecting the DC Power and Power Button**

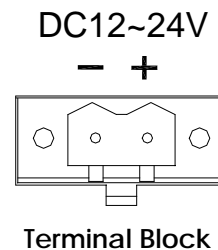
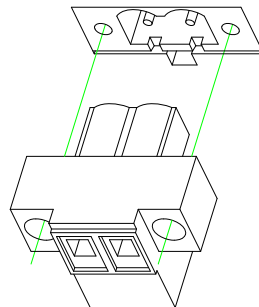
Power is supplied through an external AC/DC power adapter or power DC In. Check the technical specification section for information about AC/DC power input voltage. See following figure and a side pictures.

1. DC-Power Jack: External AC/DC power adapter plug into DC-jack (2) and use the cable mount to fix hole (1), then pull at cable(3).



DC +12V~+24V, 65VA minimum

2. DC Power Connector: Use external 2-pin apartable terminal block.



3. Power Button & Reset Push Button: Pushing the Power button once will switch the FX5636 on and off, And Reset push button is switcher for system reset; Push and release the button will cause hard ware reset of FX5636 and restart system booting.



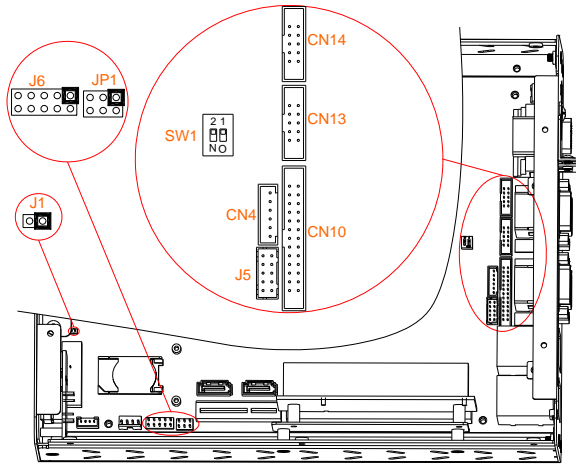
Power button: On/Off



Reset Push Button: Restart

□ **Internal Connector and Jumper Setting**

The COM 2 is designed for multiple proposes, Use SW1 of the CPU board selects RS-232, RS422 or RS-485 of COM2, , J1 is used to select clear CMOS data, and other internal connectors is reserved for user use when need to that.



**a. SW1:COM2 R232/422/485 Selection**

SW1-1	SW1-2	Signal
Off	Off	RS-232
Off	On	RS-422
On	Off	RS-485
On	On	BIOS Cntl.

Factory Preset: RS232

COM2	RS-232	RS-422	RS-485
1	-DCD		
6	-DSR		
2	RXD	RX-	485-
7	-RTS	TX-	
3	-TXD	RX+	485+
8	-CTS	TX+	
4	-DTR		
9	-RI		
5	Ground		
Metal	Case Ground		

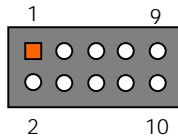
**b. J1: Clear CMOS Setting**

You can use J1 to clear CMOS data. The CMOS stores information like system date, time, boot up device, password, IRQ... which are set up with the BIOS. To clear the CMOS, set J1 to close and then open before system powers off. The default setting is opened.

J1

Factory Preset
 Clear CMOS Data

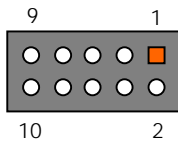
c. J5: TTL I/O Header (IDC 2.0mm)



J5	TTL Lines	J5	TTL Lines
1	GP7	6	GP2
2	GP6	7	GP1
3	GP5	8	GP0
4	GP4	9	Ground
5	GP3	10	Ground

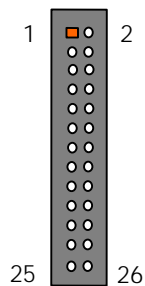
**Note:** Please refer to Chapter4 - J5 TTL I/O -Software programming example

d. J6: Internal USB Header (2.54mm)



J6	Signal	J6	Signal
1	USBV0	2	Case Ground
3	USBD0-	4	USBG1
5	USBD0+	6	USBD1+
7	USBG0	8	USBD1-
9	Case Ground	10	USBV1

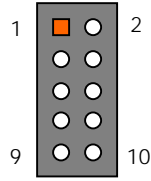
e. CN10: Parallel Port Header (2.0mm)



CN10	DB-25	Signal	CN10	DB-25	Signal
1	1	-STROBE	2	14	-AUTO FORM FEED
3	2	DATA 0	4	15	-ERROR
5	3	DATA 1	6	16	-INITIALIZE
7	4	DATA 2	8	17	-PRINTER SELECT IN
9	5	DATA 3	10	18	Ground
11	6	DATA 4	12	19	Ground
13	7	DATA 5	14	20	Ground
15	8	DATA 6	16	21	Ground
17	9	DATA 7	18	22	Ground
19	10	-ACKNOWLEDGE	20	23	Ground
21	11	BUSY	22	24	Ground
23	12	PAPER	24	25	Ground
25	13	PRINTER SELECT	26	--	No Used



f. CN13/CN14: COM5 & COM6 RS232 Serial Ports Header (2.0mm)



DB-9	CN13/CN14	RS-232
1	1	-DCD
6	2	-DSR
2	3	RXD
7	4	-RTS
3	5	-TXD
8	6	-CTS
4	7	-DTR
9	8	-RI
5	9	Ground
Metal	10	Case Ground

## Chapter 3 BIOS Setup

This chapter describes the BIOS setup.

### Overview

BIOS are a program located on a Flash memory chip on a circuit board. It is used to initialize and set up the I/O peripherals and interface cards of the system, which includes time, date, hard disk drive, and connected devices such as the video display, diskette drive, USB device, and the keyboard. This program will not be lost when you turn off the system.

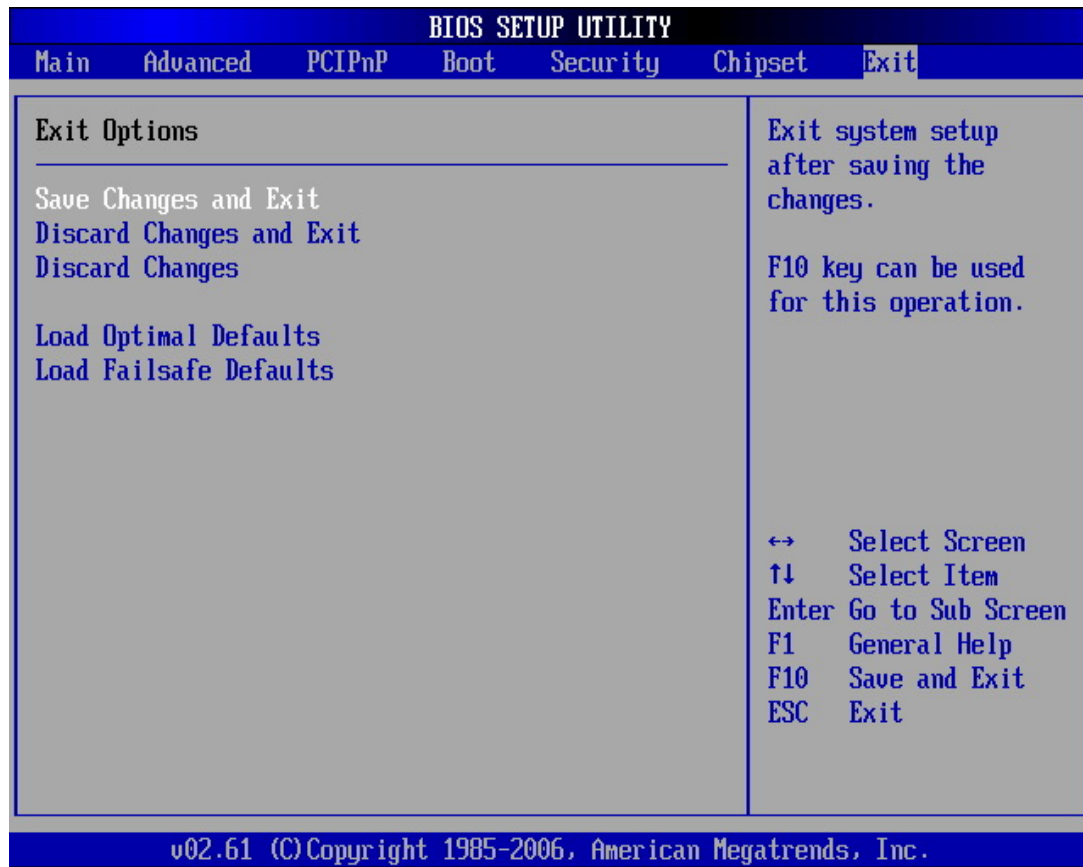
The BIOS provides a menu-driven interface to the console subsystem. The console subsystem contains special software, called firmware that interacts directly with the hardware components and facilitates interaction between the system hardware and the operating system.

The BIOS default values ensure that the system will function at its normal capability. In the worst situation the user may have corrupted the original settings set by the manufacturer.

All the changes you make will be saved in the system RAM and will not be lost after power-off.

When you start the system, the BIOS will perform a self-diagnostics test called Power On Self Test (POST) for all the attached devices, accessories, and the system. Press the [Del] key to enter the BIOS Setup program, and then the main menu will show on the screen.

**Note:** Change the parameters when you fully understand their functions and subsequence.



□ **BIOS Functions**

On the menu, you can perform the following functions

1. Main
2. Advanced
  - CPU Configuration
  - IDE Configuration
  - SuperIO Configuration
  - Hardware Health Configuration
  - ACPI Configuration
  - AHCI Configuration
  - MPS Configuration
  - SmbIOS Configuration
  - USB Configuration
3. PCIPnP
4. Boot
  - Boot Settings Configuration
  - Boot Device Priority
  - Hard Disk Drives
  - CD/DVD Drivers
5. Security
  - Change Supervisor Password
  - Change User Password

- Clear User Password
- Boot Sector Virus Protection
- 6. Chipset
  - North Bridge Configuration
  - South Bridge Configuration
- 7. Exit
  - Save Changes and Exit: Exit system setup after saving the changes. F10 key can be used for this operation.
  - Discard Changes and Exit: Exit system setup without saving any changes. ESC key can be used for this operation.
  - Discard Changes: Discard changes down so far any of the set questions. F7 key can be used this operation.
  - Load Optimized Default: to auto configure the system according to optimal setting with pre-defined values. This is also the factory default setting of the system when you receive the board.
  - Load Fail-Safe Default: to configure the system in fail-safe mode with predefined values.

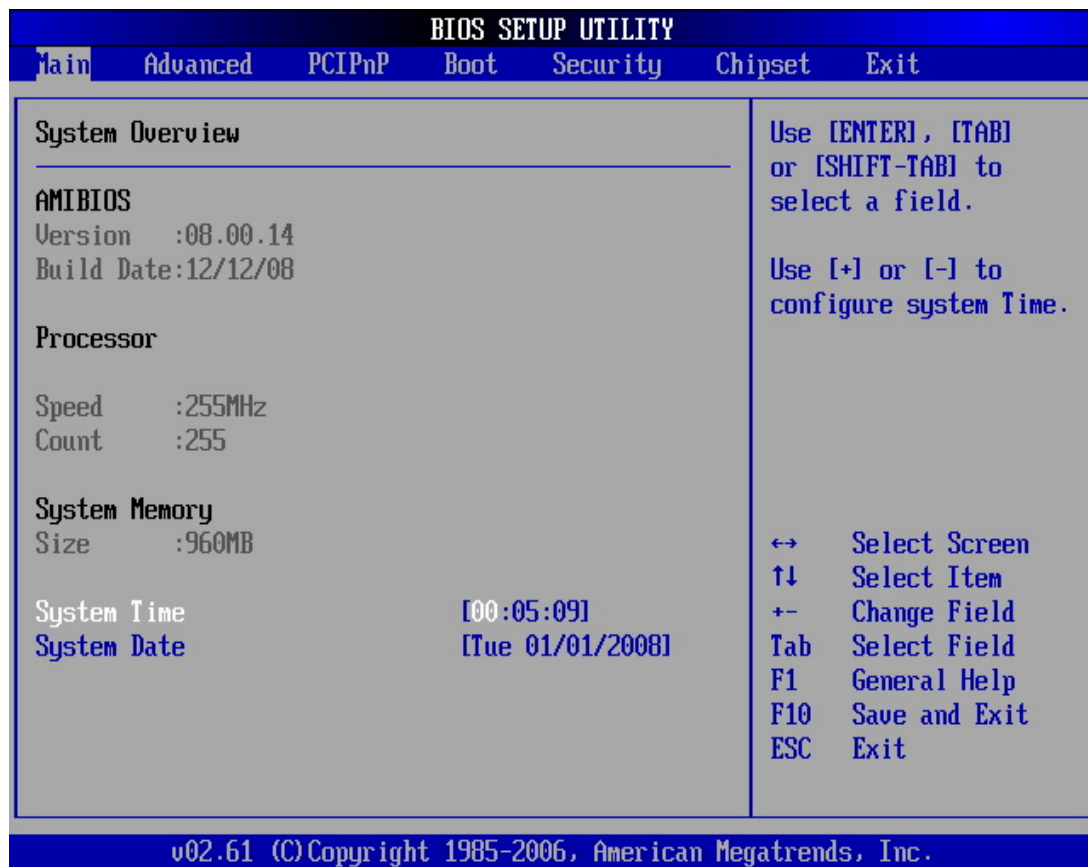
### Keyboard Convention

On the BIOS, the following keys can be used to operate and manage the menu:

Item	Function
ESC	To exit the current menu or message
Page Up/Page Down	To select a parameter
F1	To display the help menu if you do not know the purpose or function of the item you are going to configure
F8	Fail-Safe Default
F9	Optimized Default
F10	Save and exit
UP/Down Arrow Keys	To go upward or downward to the desired item

## Main Setup

This section describes basic system hardware configuration, system clock setup and BIOS version information. If the CPU board is already installed in a working system, you will not need to select this option anymore.



### System Memory

This option is display-only which is determined by POST (Power On Self Test) of the BIOS.

### System Date & Time Setup

Highlight the <Date> field and then press the [Page Up] / [Page Down] or [+]/ [-] keys to set the current date. Follow the month, day and year format.

Highlight the <Time> field and then press the [Page Up] / [Page Down] or [+]/ [-] keys to set the current date. Follow the hour, minute and second format.

The user can bypass the date and time prompts by creating an AUTOEXEC.BAT file. For information on how to create this file, please refer to the MS-DOS manual.

## Advanced Setup

Select the *Advanced* tab from the setup screen to enter the Advanced BIOS Setup screen. You can select any of the items in the left frame of the screen, such as SuperIO Configuration, to go to the sub menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS Setup options are described in this section. The Advanced BIOS Setup screen is shown below. The sub menus are described on the following pages



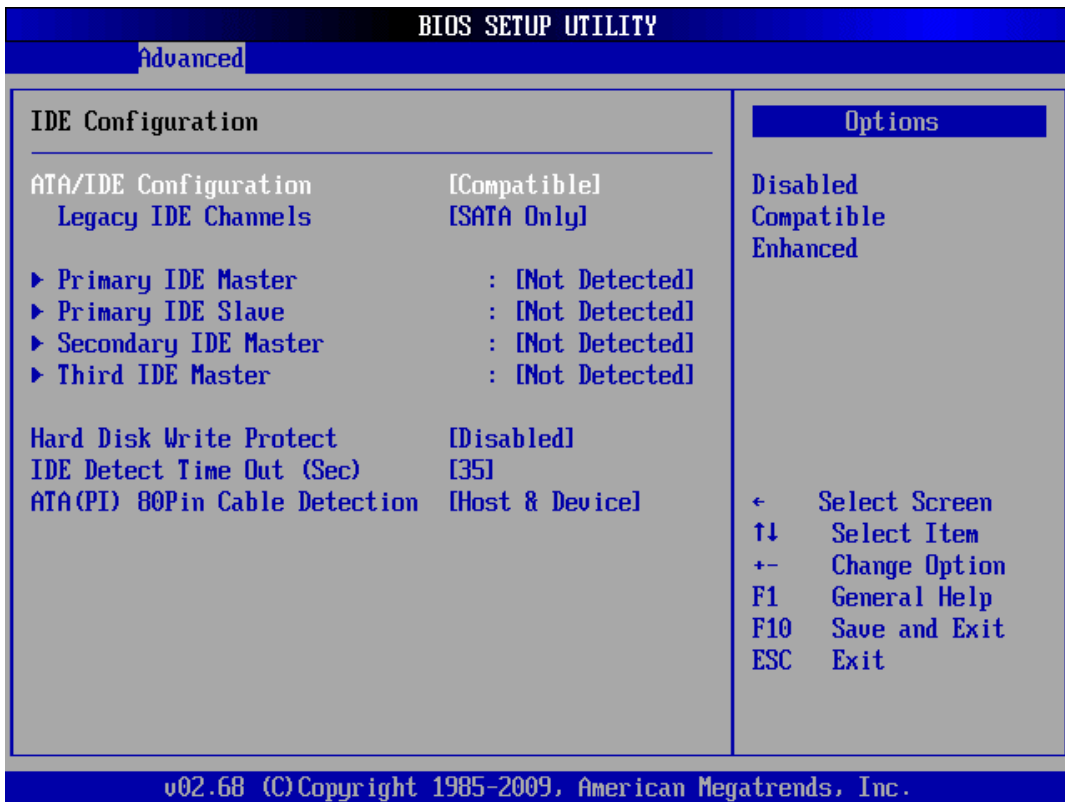
### □ CPU Configuration

You can use this screen to select options for the CPU information. Use the up and down <Arrow> keys to select an item. Use the <Plus> and <Minus> keys to change the value of the selected option.

**Note:** The CPU Configuration setup screen varies depending on the installed processor.

□ **IDE Configuration**

You can use this screen to select options for the IDE Configuration Settings. Use the up and down <Arrow> keys to select an item. Use the <Plus> and <Minus> keys to change the value of the selected option. A description of the selected item appears on the right side of the screen. The settings are described on the following pages. An example of the *IDE Configuration* screen is shown below.



**ATA/IDE Configuration**

This item specifies the ATA/IDE channels used by the onboard PATA/SATA IDE controller depend on the operation system (OS) that installed. When installed DOS, WIN95/98/ME Will can set to compatible mode. And set to Enhanced mode if you are using native OS, like Windows-XP/2k

**Available Options:** Disabled, Compatible, and Enhanced

**Default setting:** Compatible

■ **Legacy IDE Channels > Compatible Mode**

This field is when set to Compatible mode; you can select the SATA Only, SATA Pri\_PATA Sec or PATA Only.

**Available Options:** SATA Only, SATA Pri, PATA\_Sec, and PATA Only

**Default setting:** SATA Only

■ **Configure As IDE > Enhanced Mode**

This field is when set to Enhanced mode; you can select the SATA or AHCI Only.

**Available Options:** IDE and AHCI

**Default setting:** IDE

**Primary/Secondary/Third IDE Master**

IDE hard drive controllers can support up to two separate hard drives. These drives have a master/slave relationship, which is determined by the cabling configuration used to attach them to the controller. Your system supports one IDE controller – a primary – so you have the ability to install up to two separate hard disks.

**LBA/Large Mode**

LBA (Logical Block Addressing) is a method of addressing data on a disk drive. In LBA mode, the maximum drive capacity is 137 GB. The Optimal and Fail-Safe default setting is *Auto*

**Hard Disk Type**

The BIOS supports various types for user settings, The BIOS supports <Pri Master>, <Pri Slave>, so the user can install up to two hard disks. For the master and slave jumpers, please refer to the hard disk's installation descriptions and the hard disk jumper settings.

You can select <AUTO> under the <TYPE> and <MODE> fields. This will Enabled auto detection of your IDE drives during boot up. This will allow you to change your hard drives (with the power off) and then power on without having to reconfigure your hard drive type. If you use older hard disk drives, which do not support this feature, then you must configure the hard disk drive in the standard method as described above by the <USER> option.

**PIO MODE**

PIO means Programmed Input/Output. Rather than have the BIOS issue a series of commands to affect a transfer to or from the disk drive, PIO allows the BIOS to tell the controller what it wants and then let the controller and the CPU perform the complete task by them. This is simpler and more efficient (and faster). Your system supports five modes, numbered from 0 to 4, which primarily differ in timing. When Auto is selected, the BIOS will select the best available mode.

**BLOCK (Multi-Sector Transfer)**

This option allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive (HDD).

**S.M.A.R.T**

This field is used to activate the S.M.A.R.T (System Management and Reporting Technologies) function for S.M.A.R.T HDD drives. This function requires an application that can give S.M.A.R.T message.



### **32 Bit Data Transfer**

This option sets the 32-bit data transfer option. The Optimal and Fail-Safe default setting is *enabled*.

### **Hard Disk Write Protect**

Set this option to protect the hard disk drive from being overwritten. The Optimal and Fail-Safe default setting is *disabled*.

**Available Options:** Disabled, Enabled

**Default setting:** Disabled

### **IDE Detect Time Out (Sec.)**

Set this option to stop the AMIBIOS from searching for IDE devices within the specified number of seconds. Basically, this allows you to fine-tune the settings to allow for faster boot times. Adjust this setting until a suitable timing that can detect all IDE disk drives attached is found.

**Available Options:** 0, 5, 10, 15, 20, 25, 30, and 35

**Default setting:** 35

### **ATA (PI) 80 Pin Cable Detection**

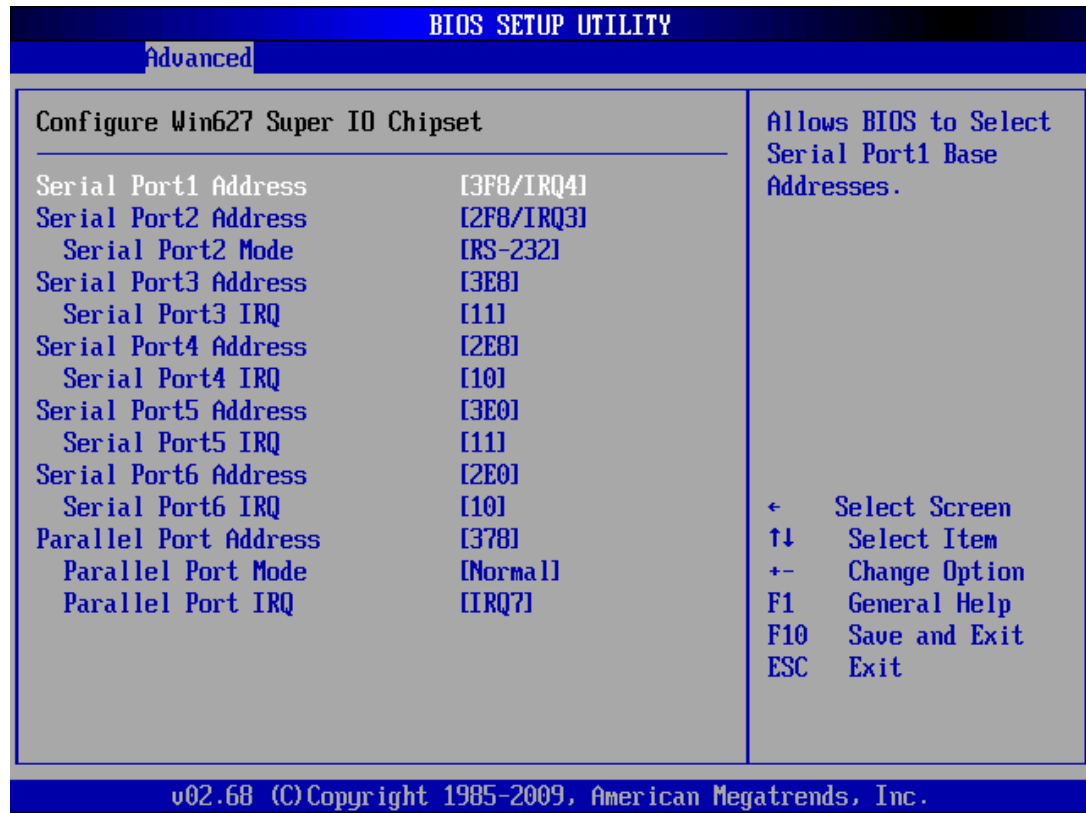
Set this option to select the method used to detect the ATA (PI) 80 pin cable. The use of an 80-conductor ATA cable is mandatory for running Ultra ATA/66, Ultra ATA/100 and Ultra ATA/133 IDE hard disk drives. The standard 40-conductor ATA cable cannot handle the higher speeds.

**Available options:** Host & Device, Host, and Device

**Default setting:** Host & Device

❑ **SuperIO Chipset - Configuration Win627 Super IO Chipset**

This section describes the function of Super I/O settings.



**Serial Port 1 Address**

These fields select the I/O port address for each Serial port.

**Available Options:** Disabled, 3F8H/IRQ4, 3E8H/IRQ10, and 2E8H/IRQ11.

**Default setting:** 3F8H/IRQ4

**Serial Port 2 Address**

These fields select the I/O port address for each Serial port.

**Available Options:** Disabled, 3E8H/IRQ10, 2F8H/IRQ4 and 2E8H/IRQ11.

**Default setting:** 2F8H/IRQ3

■ **Serial Port2 Mode Selection**

These fields item can select RS-232, RS-422 and RS-485 of select port 2.

**Available Options:** RS-232, RS-422 and RS485

**Default setting:** RS-232

### **Serial Port 3 Address**

These fields select the I/O port address for each Serial port.

**Available Options:** Disabled, 3E8H, and 2E8H.

**Default setting:** 3E8H

#### ■ *Serial Port3 IRQ*

These fields item can select the IRQ of Serial port 3.

**Available Options:** 10 and 11

**Default setting:** 11

### **Serial Port 4 Address**

These fields select the I/O port address for each Serial port.

**Available Options:** Disabled, 3E8H, and 2E8H.

**Default setting:** 2E8H

#### ■ *Serial Port4 IRQ*

These fields item can select the IRQ of Serial port 4.

**Available Options:** 10 and 11

**Default setting:** 10

### **Serial Port 5 Address (Reserved)**

These fields select the I/O port address for each Serial port.

**Available Options:** Disabled, 3E0H, and 2E0H.

**Default setting:** 3E0H

#### ■ *Serial Port5 IRQ*

These fields item can select the IRQ of Serial port 5.

**Available Options:** 10 and 11

**Default setting:** 11

### Serial Port 6 Address (Reserved)

These fields select the I/O port address for each Serial port.

**Available Options:** Disabled, 3E0H, and 2E0H.

**Default setting:** 2E0H

#### ■ *Serial Port6 IRQ*

These fields item can select the IRQ of Serial port 6.

**Available Options:** 10 and 11

**Default setting:** 10

### Parallel Port Address (Reserved)

This field selects the I/O port address for parallel port.

**Available Options:** Disabled, 378H, 278H, and 3BCH

**Default setting:** 378H Parallel Port Mode

This field specifies the parallel port mode. ECP and EPP are both bi-directional data transfer schemes that adhere to the IEEE P1284 specifications.

**Available Options:** Normal, Bi-Dir, ECP, EPP, and ECP+EPP

**Default setting:** Normal

### Parallel Port IRQ

This field specifies the IRQ for the parallel port.

**Available Options:** IRQ5 and IRQ7

**Default setting:** IRQ7

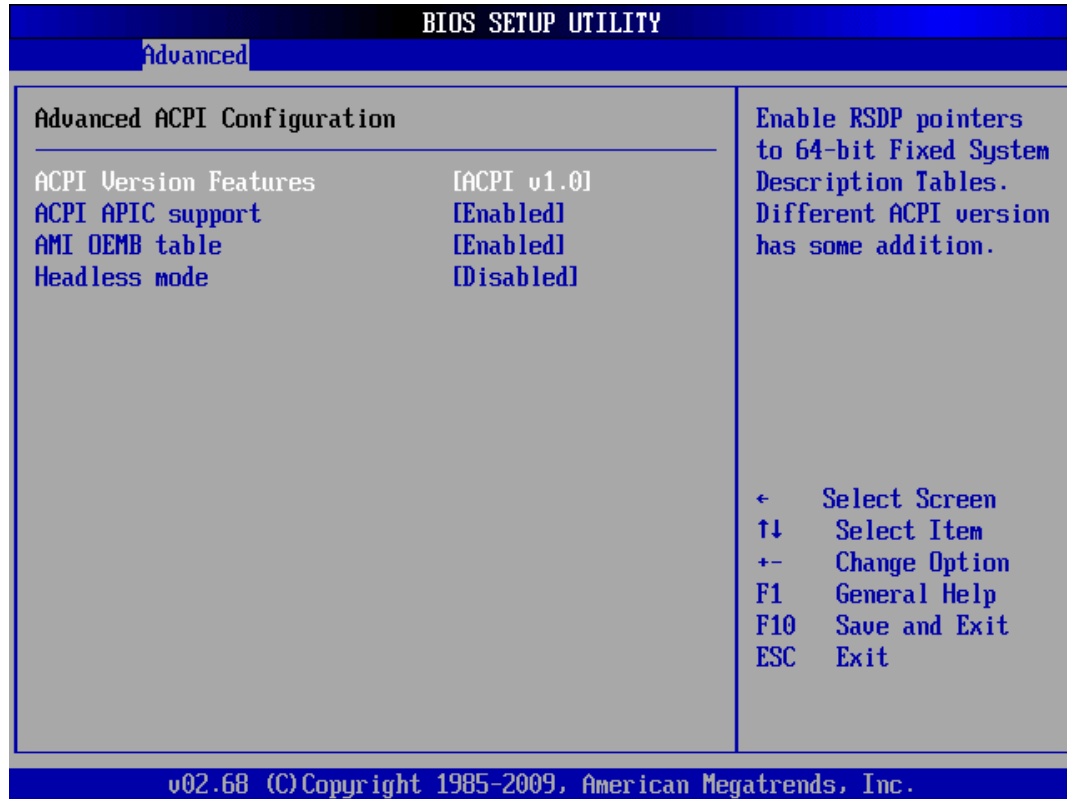
□ **Hardware Health Configuration**

On the Hardware Monitor Setup screen, you can monitor the system temperature, CPU voltage, and VCC voltage...

BIOS SETUP UTILITY		
Advanced		
<b>Hardware Health Configuration</b>		Enables Hardware Health Monitoring Device.
H/W Health Function	[Enabled]	
<b>Hardware Health Event Monitoring</b>		← Select Screen ↑↓ Select Item +- Change Option F1 General Help F10 Save and Exit ESC Exit
SYSTEM Temperature	:26°C/78°F	
CPU Core1 Temperature	:50°C/122°F	
CPU Core2 Temperature	:52°C/125°F	
Fan3 Speed	:2860 RPM	
CPU UCORE	:1.064 V	
UCC3.3	:3.370 V	
UCC1.8	:1.854 V	
UCC1.5	:1.548 V	
UCCP1.05	:1.048 V	
v02.68 (C) Copyright 1985-2009, American Megatrends, Inc.		

□ **Advanced ACPI Settings**

This field specifies allow you set this value to utilize the ACPI (Advanced Configuration and Power Interface) specification.



**Advanced ACPI Configuration**

*ACPI Version Feature*

Set this value to allow or prevent the system to be complaint with the ACPI Specification.

**Available Options:** ACPIv1.0, ACPIv2.0, and ACPIv3.0

**Default setting:** ACPIv1.0

■ **APIC ACPI SCI IRQ**

Allows you to enable or disable the internal I/O APIC and Multiprocessor Tables. (Disable the APIC ACPI SCI IRQ may require the O.S. to be reinstalled)

**Available Options:** Disabled, and Enabled

**Default setting:** Disabled

■ **AMI OEMB Table**

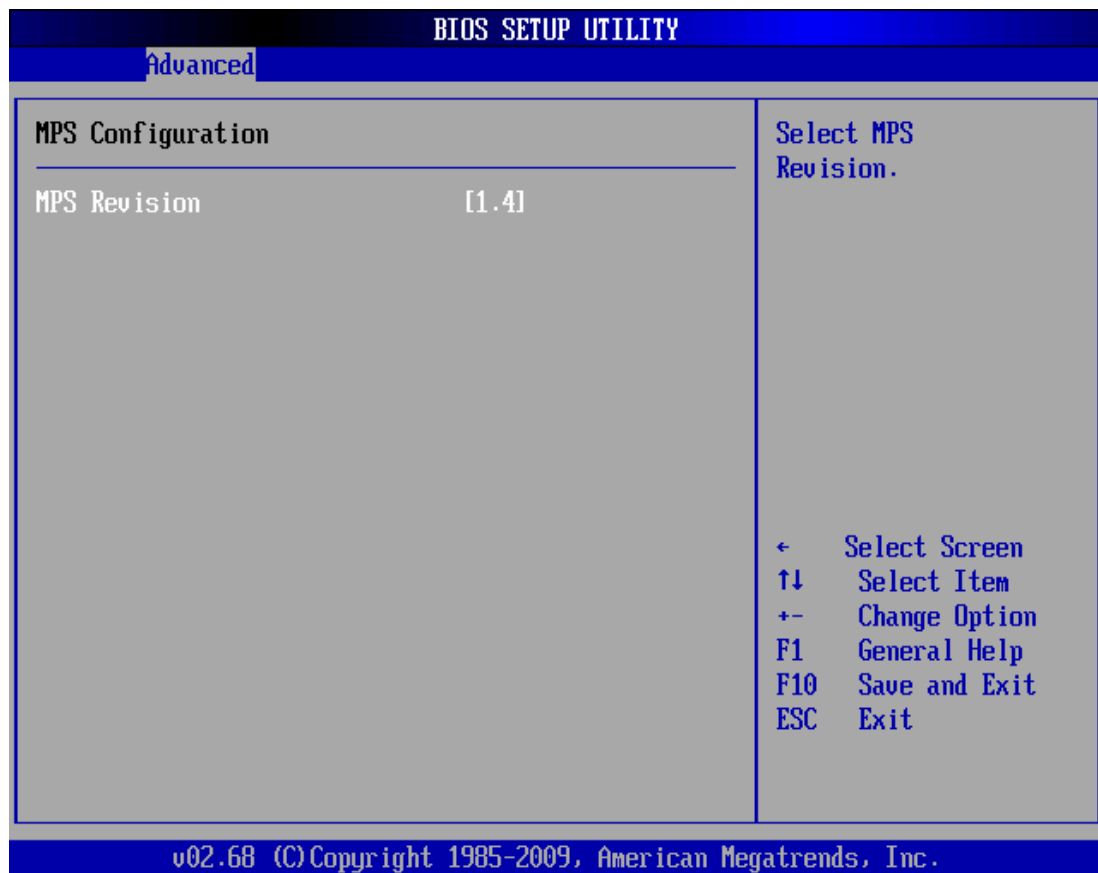
Set this value to allow the ACPI BIOS to add a pointer to an OEMB table in the Root System Description Table (RSDT) table.

**Available Options:** Enabled, and Disabled

**Default setting:** Enabled

□ **MPS Configuration**

This field specifies allow you select the MPS (Multi Processor Specification) Revision.



**MPS Revision**

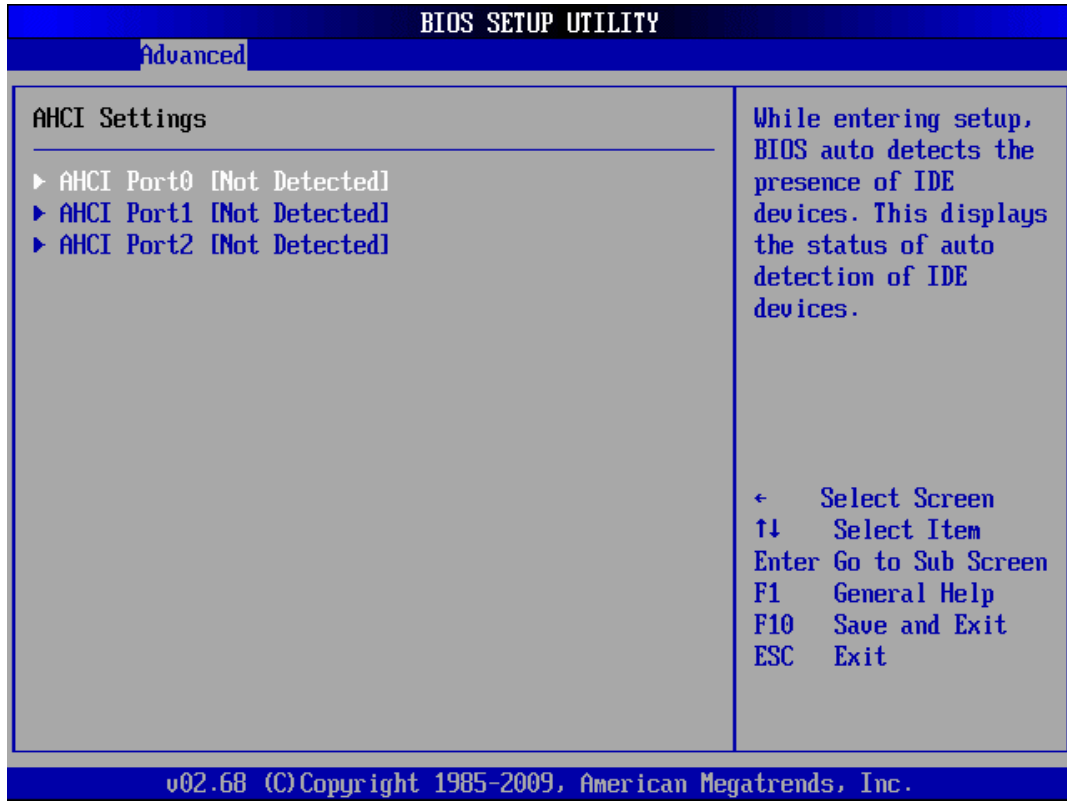
This field is select MPS Revision.

**Available Options:** 1.1, and 1.4

**Default setting:** 1.4

□ **AHCI Configuration**

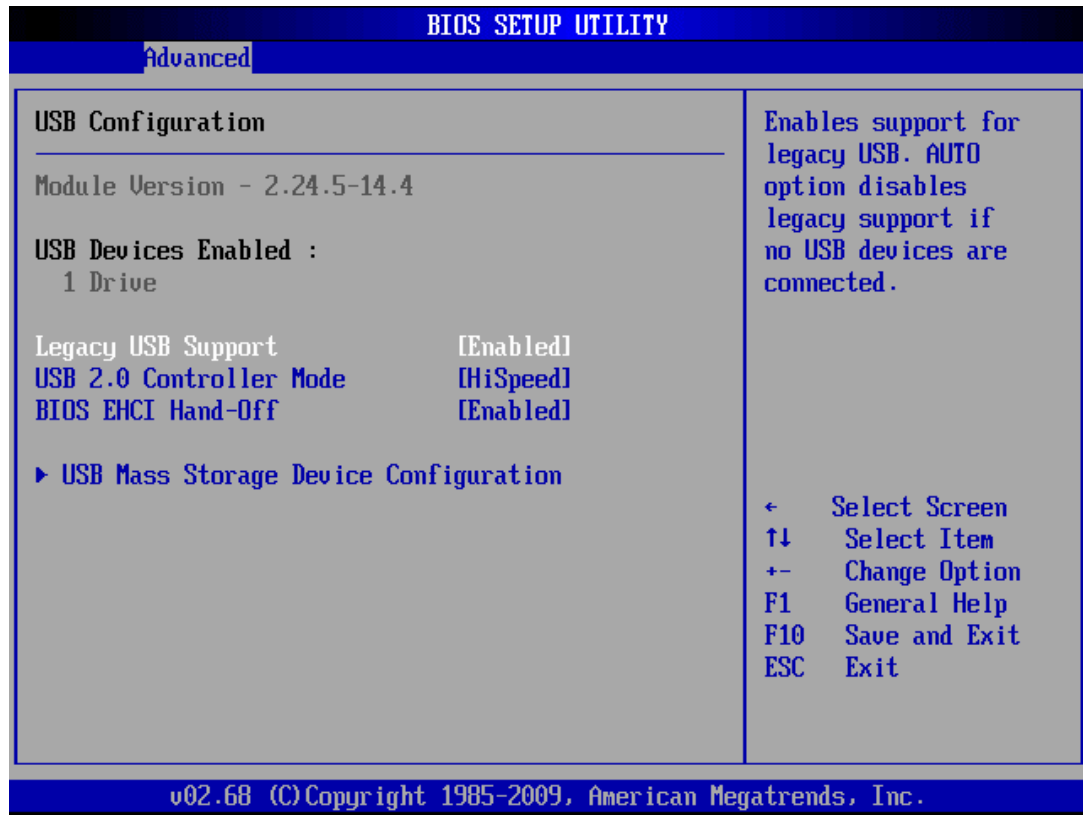
This field is when SATA is set to AHCI mode, you can use this screen to select options for the AHCI SATA port settings.





□ **USB Configuration**

You can use this screen to select options for the USB Configuration.



**Legacy USB Support**

Legacy USB Support refers to the USB mouse and USB keyboard support. Normally if this option is not enabled; any attached USB mouse or USB keyboard will not become available until a USB compatible operating system is fully booted with all USB drivers loaded. When this option is enabled, any attached USB mouse or USB keyboard can control the system even when there is no USB drivers loaded on the system. Set this value to Enabled or Disabled the Legacy USB Support.

**Available Options:** Disabled, Auto, and Enabled

**Default setting:** Enabled

**USB 2.0 Controller Mode**

This field is configures the USB 2.0 controllers in High speed (480Mbps) or Full speed (12Mbps).

**Available Options:** HiSpeed, and FullSpeed

**Default setting:** FullSpeed

### **BIOS ECHI Hand-Off**

This is a workaround for OS without ECHI Hand-Off support. The ECHI ownership change should claim by ECHI driver.

**Available Options:** Enabled, and Disabled

**Default setting:** Enabled

## PCIPnP Setup

Select the *PCI/PnP* tab from the setup screen to enter the Plug and Play BIOS Setup screen. You can display a Plug and Play BIOS Setup option by highlighting it using the <Arrow> keys. All Plug and Play BIOS Setup options are described in this section. The Plug and Play BIOS Setup screen is shown below.



### Clear NVRAM

Clear NVRAM during system boot.

**Available Options:** Yes, No

**Default setting:** No

### Plug and Plug Aware O/S

Set to Yes to inform BIOS that the operating system can handle Plug and Play (PnP) devices.

**Available Options:** Yes, No

**Default setting:** No

### **PCI Latency Timer**

This field specifies the latency timings (in PCI clock) PCI devices installed in the PCI expansion bus.

**Available Options:** 32, 64, 96, 128, 160, 192, 224, and 248

**Default setting:** 64

### **PCI IDE BusMaster**

This option is to specify that the IDE controller on the PCI local bus have bus-mastering capability.

**Available Options:** Enabled, Disabled

**Default setting:** Disabled

### **IRQ 3 –15**

When I/O resources are controlled manually, you can assign each system interrupt as one of the following types, based on the type of device using the interrupt:

Available: Specified IRQ is available to be used by PCI/PnP devices.

Reserved: Specified IRQ is reserved for use by Legacy ISA devices.

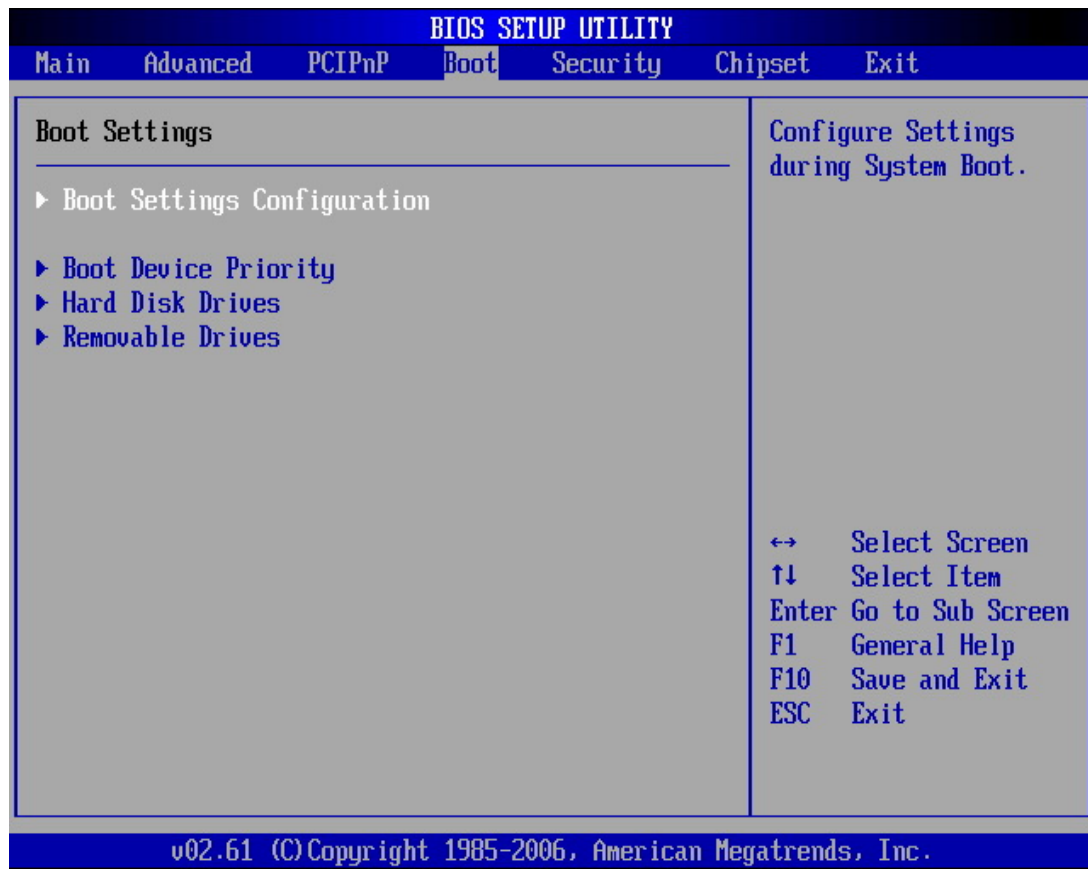
**Available Options:** Available and Reserved

**Default setting:** Available

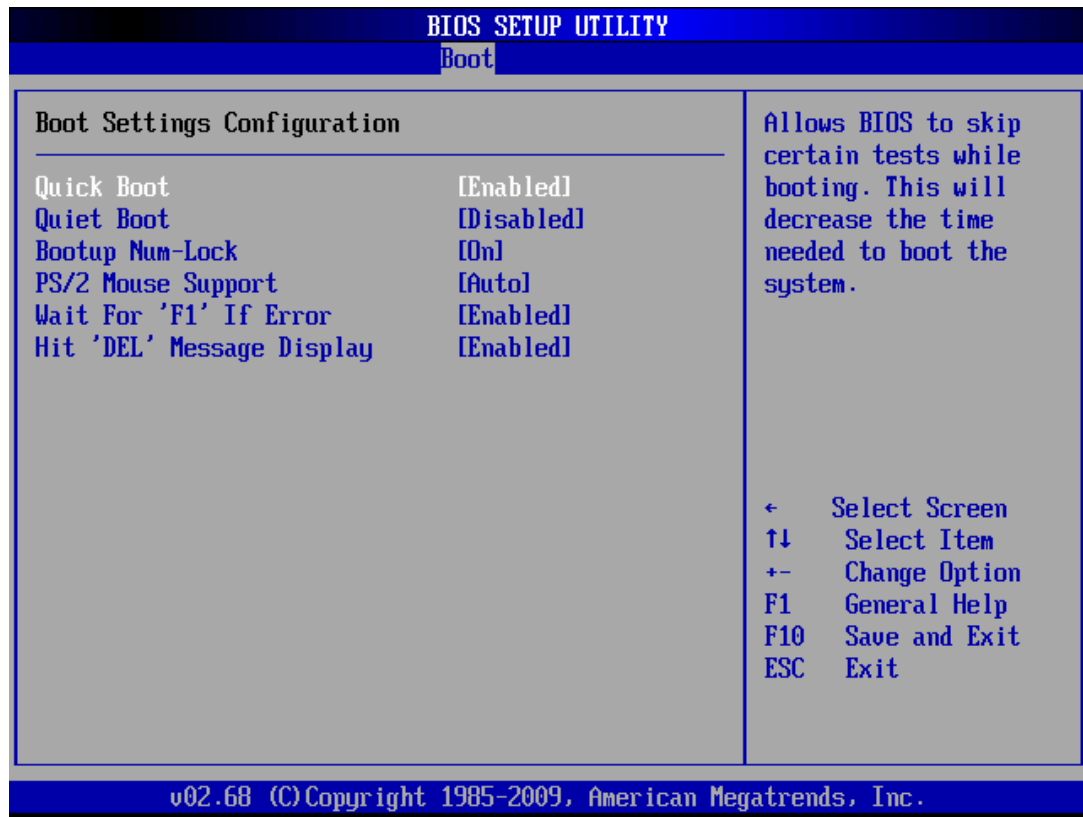
## Boot Setup

Select the *Boot* tab from the setup screen to enter the Boot BIOS Setup screen. You can select any of the items in the left frame of the screen, such as Boot Device Priority, to go to the sub menu for that item. You can display a Boot BIOS Setup option by highlighting it using the <Arrow> keys. All Boot Setup options are described in this section. Select an item on the Boot Setup screen to access the sub menu for:

- Boot Setting Configuration
- Boot Device Priority
- Hard disk drives
- Removable Drivers



□ Boot Setting Configuration



**Quick Boot**

This field is used to activate the quick boot function of the system. When set to Enabled,

1. BIOS will not wait for up to 40 seconds if a Ready signal is not received from the IDE drive, and will not configure its drive.
2. BIOS will not wait for 0.5 seconds after sending a RESET signal to the IDE drive.
3. You cannot run BIOS Setup at system boot since there is no delay for the Hit, Del. To run Setup message.

**Available Options:** Disabled, Enabled

**Default setting:** Enabled

**Boot Up Num-Lock**

This field is used to activate the Num Lock function upon system boot. If the setting is on, after a boot, the Num Lock light is lit, and user can use the number key.

**Available options:** On, Off

**Default setting:** On

### **PS/2 Mouse Support**

The PS/2 mouse function is optional. Before you configure this field, make sure your system board supports this feature. The setting of ***Enabled*** allows the system to detect a PS/2 mouse on boot up. If detected, IRQ12 will be used for the PS/2 mouse. IRQ 12 will be reserved for expansion cards if a PS/2 mouse is not detected. ***Disabled*** will reserve IRQ12 for expansion cards and therefore the PS/2 mouse will not function.

**Available options:** Disabled, Enabled and Auto

**Default setting:** Auto

### **Wait for 'F1' If Error**

AMIBIOS POST error messages are followed by:

Press <F1> to continue

If this field is set to ***Disabled***, the AMIBIOS does not wait for you to press the <F1> key after an error message.

**Available options:** Disabled, Enabled

**Default setting:** Disabled

### **Hit 'DEL' Message Display**

Set this field to ***Disabled*** to prevent the message as follows:

Hit 'DEL' if you want to run setup

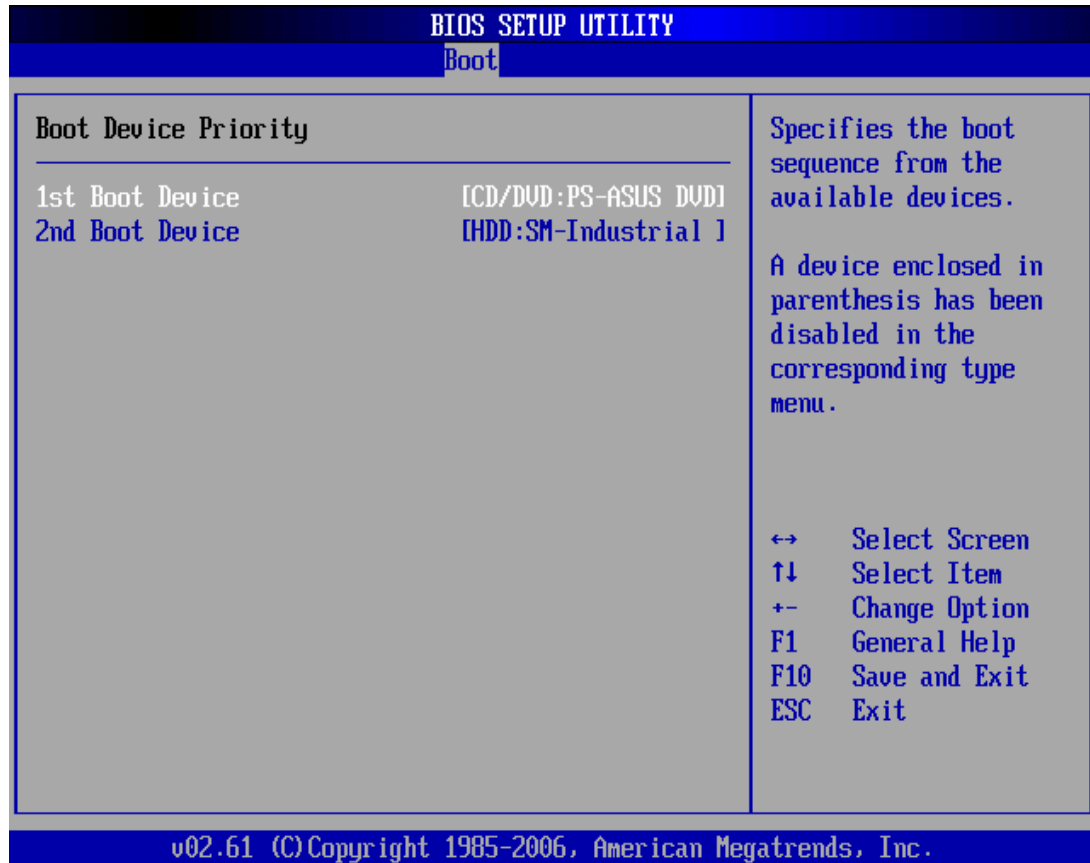
It will prevent the message from appearing on the first BIOS screen when the computer boots.

**Available options:** Disabled, Enabled

**Default setting:** Enabled

□ **Boot Device**

Use this screen to specify the order in which the system checks for the device to boot from. To access this screen, select Boot Device Priority on the Boot Setup screen and press <Enter>.



**First /Second /Third Hard Disk Boot Device**

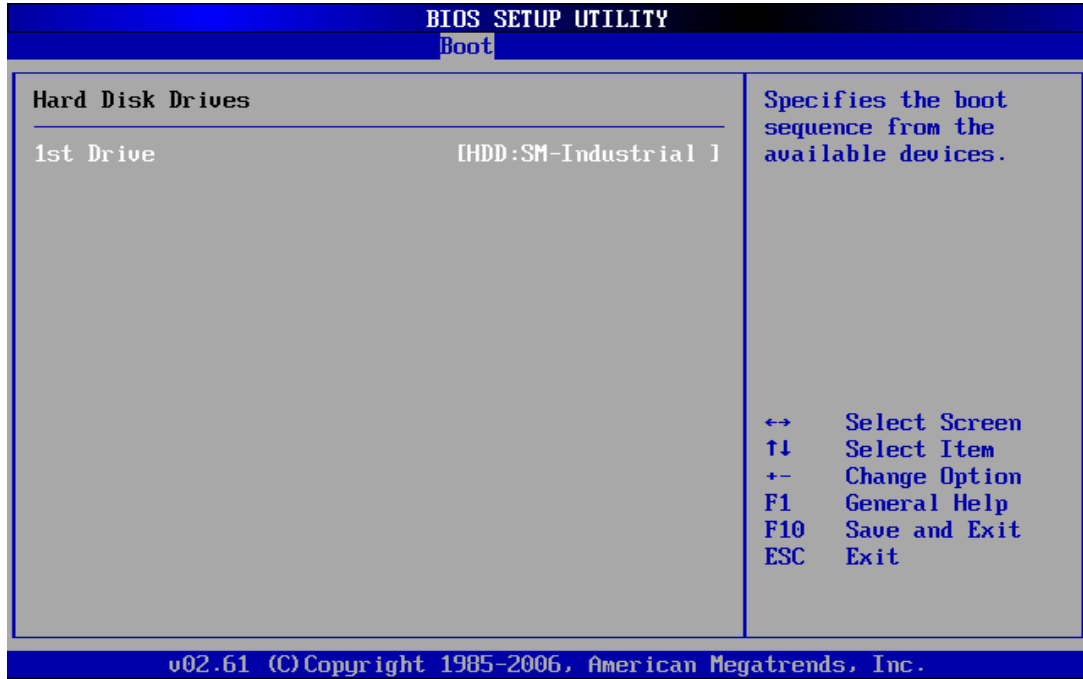
Set the boot device options to determine the sequence in which the computer checks which device to boot from. The settings are *Removable Dev.*, *Hard Drive*, or *ATAPI CDROM*.

**Note:** When you select a boot category from the boot menu, a list of devices in that category appears. For example, if the system has three hard disk drives connected, then the list will show all three hard disk drives attached



□ **Hard Disk Drives**

Use this screen to view the hard disk drives in the system. To access this screen, select Hard disk drives on the Boot Setup screen and press <Enter>.



## Security setup

There are two security passwords: Supervisor and User. Supervisor is a privileged person that can change the User password from the BIOS. According to the default setting, both access passwords are not set up and are only valid after you set the password from the BIOS.

- Change Supervisor Password
- Change User Password
- Clear User Password
- Boot Sector Virus Protection



To set the password, please complete the following steps.

1. Select **Change Supervisor Password**.
2. Type the desired password (up to 6 character length) when you see the message, "Enter New Supervisor Password."
3. Then you can go on to set a user password (up to 6 character length) if required. Note that you cannot configure the User password until the Supervisor password is set up.
4. Enter Advanced BIOS Features screen and point to the Security Option field.

5. Select System or Setup.
  - ✧ **Always:** a visitor who attempts to enter BIOS or operating system will be prompted for password.
  - ✧ **Setup:** a visitor who attempts to the operating system will be prompted for user password. You can enter either User password or Supervisor password.
6. Point to **Save Settings and Exit** and press Enter.
7. Press Y when you see the message, "Save Current Settings and Exit (Y/N)?"

Note: it is suggested that you write down the password in a safe place to avoid that password may be forgotten or missing.

### Clear User Password

Select Clear User Password from the Security Setup menu and press <Enter>. Clear New Password > [Ok] [Cancel] appears. Type the password and press <Enter>. The screen does not display the characters entered. Retype the password as prompted and press <Enter>.

### Password Check

This field enables password checking every time the computer is powered on or every time the BIOS Setup is executed. If **Always** is chosen, a user password prompt appears every time and the BIOS Setup Program executes and the computer is turned on. If **Setup** is chosen, the password prompt appears if the BIOS executed.

**Available options:** Setup, Always

**Default setting:** Setup

### Boot Sector Virus Protection

This option is near the bottom of the Security Setup screen. The Optimal and Fail-Safe default setting is *disabled*

Enabled: Set this value to prevent the Boot Sector Virus Protection. This is the default setting.

Disabled: Select Enabled to enable boot sector protection, displays a warning when any program (or virus) issues a Disk Format command or attempts to write to the boot sector of the hard disk drive. If enabled, the following appears when a write is attempted to the boot sector. You may have to type N several times to prevent the boot sector write. Boot Sector Write!

Possible VIRUS: Continue (Y/N)? \_

The following appears after any attempt to format any cylinder, head, or sector of any hard disk drive via the BIOS INT 13 Hard disk drive Service:

Format!!!

Possible VIRUS: Continue (Y/N)?

## Chipset Setup

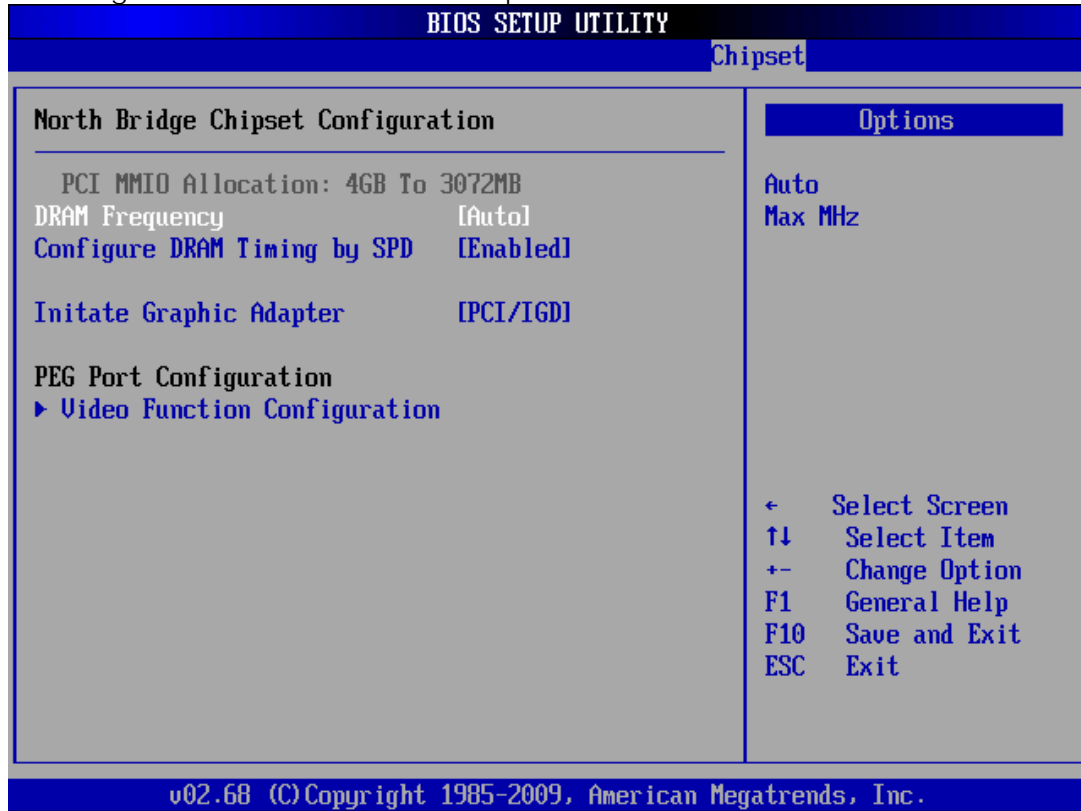
This section describes the configuration of the board's chipset features.

- North Bridge Configuration
- South Bridge Configuration



❑ **North Bridge Configuration**

You can use this screen to select options for the North Bridge Configuration. Use the up and down <Arrow> keys to select an item. Use the <Plus> and <Minus> keys to change the value of the selected option.



**DRAM Frequency**

This specifies the DDRIII SDRAM memory clock frequency.

**Available Options:** Auto, and Max MHz

**Default setting:** Auto

**Configure DRAM Timing by SPD**

SPD represents Serial Presence Detect. It is an 8-bit, 2048 bits EEPROM, built on the SDRAM for 800 MHz frequencies. If the installed DDRIII SDRAM supports SPD function, select SPD. If not, you can select based on other access time of the SDRAM.

**Available Options:** Disabled and Enabled

**Default setting:** Enabled

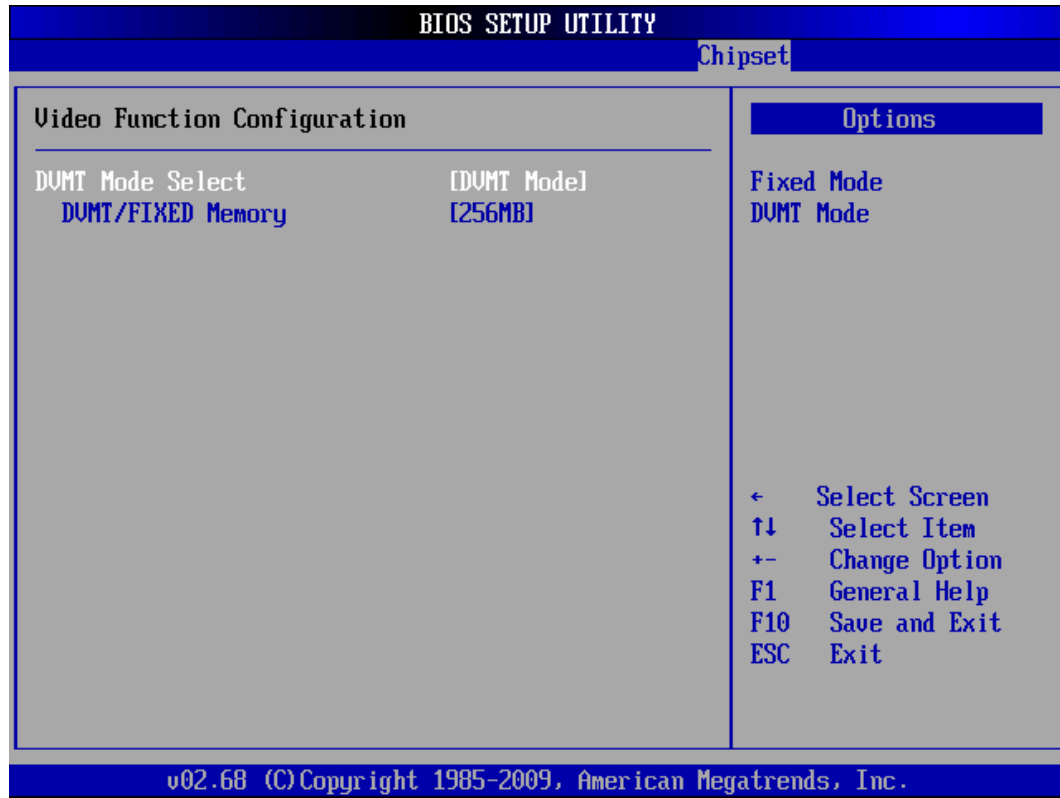
**Initate Graphic Adapter**

This field is select which graphics controller to use as the primary boot device.

**Available Options:** IGD, PCI/IGD, PCI/PEG, PEG/IGD, and PCI/IGD

**Default setting:** PCI/IGD

**Video Function Configuration**



■ **DVMT Mode Select**

This field is the *DVMT mode* is memory that is dynamically allocated based on memory requests made by application and are released back to the system once the requesting application has been terminated. *Fixed mode* is non-contiguous page locked memory allocated during driver initialization to provide a static amount of memory.

**Available Options:** Fixed Mode, and DVMT Mode

**Default setting:** DVMT Mode

■ **DVMT/FIXED Memory**

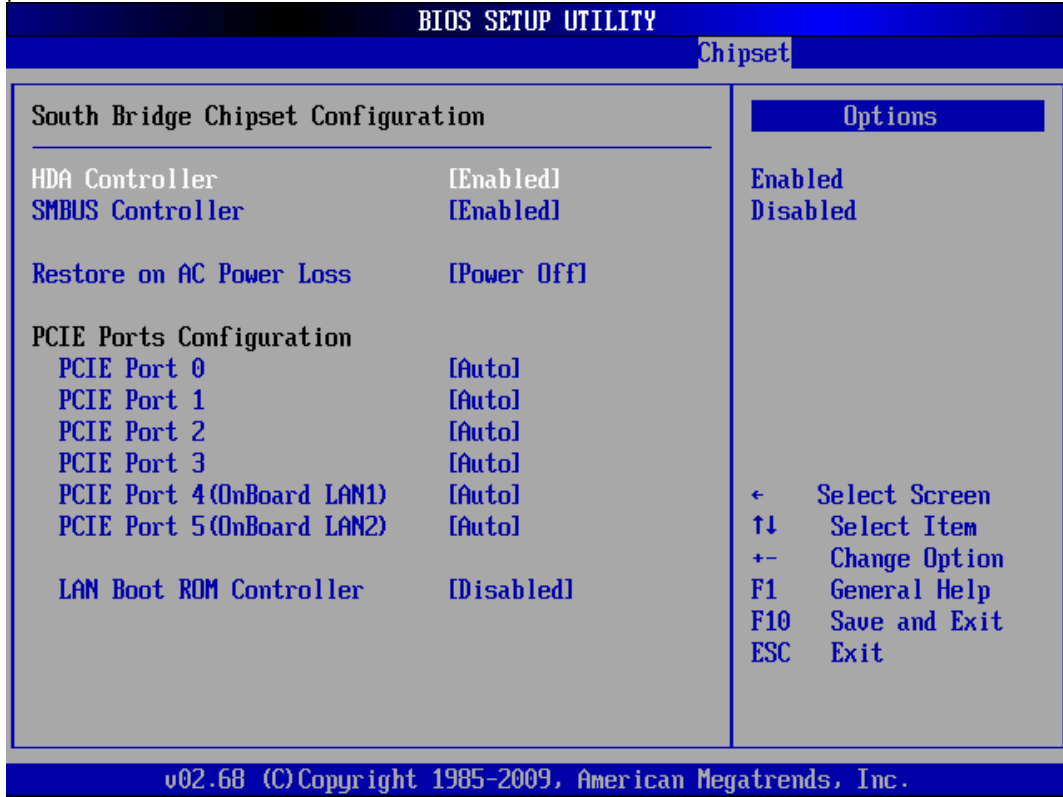
This field specifies allows you to select the maximum amount of graphics memory to be shared with the system memory.

**Available Options:** 128MB, 256MB, and Maximum DVMT

**Default setting:** 256MB

➤ **South Bridge Configuration**

You can use this screen to select options for the South Bridge Configuration. South Bridge is a chipset on the motherboard that controls the basic I/O functions, LAN port, and audio function.



**HDA Controller**

This field specifies the internal Audio Control.

**Available Options:** Disabled and Enabled

**Default setting:** Enabled

**SMBUS Controller**

This field is select Enabled or Disable the SMBUS controllers

**Available Options:** Disabled, Enabled

**Default setting:** Enabled

### **Restore On AC Power Lose**

This field specifies the option controls how the PC will behave once power is restored following a power outage (or other unexpected or ungraceful shutdown). The "Last State" option returns the PC to the state in effect at the time the power outage or shutdown occurred. Assign this option the "Power On" value to reboot automatically; assign the "Power Off" value to leave the machine powered down.

**Available Options:** Power Off, Power On, and Last State

**Default setting:** Power Off

### **PCIE Port 0~3 (Reserved for PCIe Mini-Card and PCIe-BUS)**

This field specifies the Enabled or Disabled of the onboard PCIe.

**Available Options:** Auto, Disabled, and Enabled

**Default setting:** Auto

### **PCIE Port 4/5 (On Board LAN1/LAN2)**

This field specifies the Enabled or Disabled of the onboard PCIE LAN chip.

**Available Options:** Auto, Disabled, and Enabled

**Default setting:** Auto

#### ■ ***LAN Boot ROM Controller***

This field specifies the PXE boot ROM of the onboard LAN chip.

**Available Options:** Disabled, and Enabled

**Default setting:** Disabled





## Chapter 4 Software Installation

The enclosed CD diskette includes FX5636 VGA, Audio, System, and LAN driver. To install and configure you FX5636 system, you need to perform the following steps.

### System Driver

#### Windows XP/7- System Driver

Installs Atom D525 Chipset, Core PCI, PCIe, SATA, USB, ISAPnP and IDE/ATA Device Drive.

- Step 1: To install the Atom D525 driver, insert the CD ROM into the CD ROM device, and enter DRIVER>SysChip>ICH8M.
- Step 2: Execute Setup.exe file.
- Step 3: The screen shows the SETUP type. Press any key to enter the main menu.
- Step 4: As the setup is completed, the system will generate the message as follows.

Yes, I want to restart my computer now. Installation is done!

No, I will restart my computer later.

System must be restart then complete the installation.

**Note:** In the Syschip>ICH8M directory, a Readme.txt file is included to provide installation information.

## VGA Driver

### WIN XP/7 Driver

- Step 1: To install the VGA driver, insert the CD ROM into the CD ROM device, and enter DRIVER>VGA>Atom-D>WIN7 or >WINXP.
- Step 2: Execute SETUP.EXE file.
- Step 3: The screen shows the SETUP type. Press any key to enter the main menu.
- Step 4: As the setup is completed, the system will generate the message as follows.  
Yes, I want to restart my computer now. Installation is done!  
No, I will restart my computer later.  
System must be restart then complete the installation.
- Step 5: In the WINDOWSXP, you can find the <DISPLAYL> icon located in the {CONTROL PANEL} group.
- Step 6: Adjust the <Refresh Rate>, <Font size> and <Resolution>.

**Note:** In the DRIVER>VGA>Atom-D>WIN7 or WINXP directory, a Readme.txt file is included to provide installation information.

## Audio Driver

### WIN XP/7 Driver

- Step 1: To install the AUDIO driver, insert the CD ROM into the CD ROM device, and enter DRIVER>AUDIO>ALC888>WIN2K&XP or >WIN7.
- Step 2: Execute SETUP.exe file.
- Step 3: The screen shows the SETUP type. Press any key to enter the main menu.
- Step 4: As the setup is completed, the system will generate the message as follows.  
Yes, I want to restart my computer now. Installation is done!  
No, I will restart my computer later.  
System must be restart then complete the installation.

**Note:** In the DRIVER>AUDIO>ALC888>WIN2K&XP or >WIN7 directory, a Readme.txt file is included to provide installation information.

## **LAN Driver (RTL 8111C)**

### **WINXP/7 Driver**

- Step 1: To install the LAN driver, insert the CD ROM into the CD ROM device, and enter DRIVER>LAN>RTL8111C. If your system is not equipped with a CD ROM device, copy the LAN driver from the CD ROM to CF.
- Step 2: Execute setup.exe file.

## **BIOS Flash Utility**

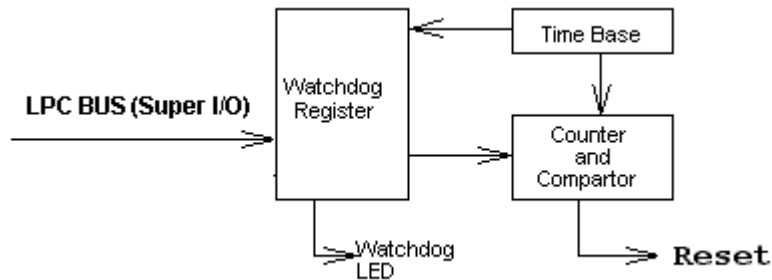
In the <UTILITY> directory, there is the Afu438.EXE file.

- Step 1: Use the Afu438.EXE program to update the BIOS setting.
- Step 2: And then refer to the chapter "BIOS Setup", as the steps to modify BIOS.
- Step 3: Now the CPU board's BIOS loaded with are the newest program; user can use it to modify BIOS function in the future, when the BIOS add some functions.

## Watchdog Timer

This section describes how to use the Watchdog Timer, including disabled, enabled, and trigger functions.

The system is equipped with a programmable time-out period watchdog timer. You can use your own program to enable the watchdog timer. Once you have enabled the watchdog timer, the program should trigger the I/O every time before the timer times out. If your program fails to trigger or disable this timer before it times out, e.g. because of a system hang-up, it will generate a reset signal to reset the system. The time-out period can be programmed to be set from 1 to 255 seconds or minutes.



### Watchdog Timer Setting

The watchdog timer is a circuit that may be used from your program software to detect system crashes or hang-ups. The watchdog timer is automatically disabled after reset.

Once you have enabled the watchdog timer, your program must trigger the watchdog timer every time before it times out. After you trigger the watchdog timer, it will be set to non-zero value to watchdog counter and start to count down again. If your program fails to trigger the watchdog timer before time-out, it will generate a reset pulse to reset the system.

The factor of the watchdog timer time-out constant is approximately 1 second. The period for the watchdog timer time-out is between 1 to FF timer factors.

If you want to reset your system when watchdog times out, the following table listed the relation of timer factors between time-out periods.

Time Factor	Time-Out Period (Seconds)	Time-Out Period (Minutes)
1	1	1
2	2	2
3	3	3
4	4	4
5	5	5
"	"	"
"	"	"
"	"	"
FF	FF	FF

### Watchdog Timer Enabled

To enable the watchdog timer, you have to output a byte of timer factor to the watchdog register whose address is 2Eh and data port is 2fH. The following is an Assemble program, which demonstrates how to enable the watchdog timer and set the time-out period at 28 seconds.

```
-----  
; Enter the extended function mode, interruptible double-write  
-----  
Mov     dx, 2eh           ; Enter to extended function mode  
Mov     al, 87h  
Out     dx, al  
Out     dx, al  
Mov     al, 07h  
Out     dx, al  
-----  
; Logical device 8, configuration register CRF5 Bit 3, CRF6 Bit 0~7  
-----  
Mov     dx, 2fh  
Mov     al, 08h           ; Select Logical Device 8 of watchdog timer  
Out     dx, al  
Mov     dx, 2eh  
Mov     al, 0f5h         ; Set second as counting unit  
Out     dx, al  
Mov     dx, 2fh  
In      al, dx  
Or      al, c0           ; Trigger P/W LED.  
And     al, not c8h      ; Set Second.  
; And   al, c8h         ; Set Minute.  
Out     dx, al  
Mov     dx, 2eh  
Mov     al, 0f6h  
Out     dx, al  
Mov     dx, 2fh  
Mov     al, 28h         ; Set timeout interval as 28seconds and start counting  
Out     dx, al  
-----  
; Exit extended function mode  
-----  
Mov     dx, 2eh  
Mov     al, 0aah  
Out     dx, al
```

### Watchdog Timer Trigger

After you enable the watchdog timer, your program must write the same factor as enabling to the watchdog register at least once every time-out period to its previous setting. You can change the time-out period by writing another timer factor to the watchdog register at any time, and you must trigger the watchdog before the new time-out period in next trigger.

### Watchdog Timer Disabled

To disable the watchdog timer, simply write a 00H to the watchdog register.

```
-----  
; Enter the extended function mode, interruptible double-write  
-----  
Mov    dx,2eh      ; Enter to extended function mode  
Mov    al,87h  
Out    dx,al  
Out    dx,al  
-----  
; Logical device 8, configuration register CRF5 Bit 3(Sec./Min.), CRF6 Bit 0~7 (Count.)  
-----  
Mov    al,07h  
Out    dx,al  
Mov    dx,2fh  
Mov    al,08h      ; Select Logical Device 8 of watchdog timer  
Out    dx,al  
Mov    dx,2eh  
Mov    al,0f5h     ;Set second as counting unit  
Out    dx,al  
Mov    dx,2fh  
In     al,dx  
And    al,not c8h  ;Set Second or Minute.  
Out    dx,al  
Mov    dx,2eh  
Mov    al,0f6h  
Out    dx,al  
Mov    dx,2fh  
Mov    al,00h     ; Set Watchdog Timer Disabled  
Out    dx,al  
-----  
; Exit extended function mode  
-----  
Mov    dx,2eh     ;Exit the extended function mode  
Mov    al,0aah  
Out    dx,al
```



## TTL I/O(GPIO) programming

The following is an Assembly program, which demonstrates how to set the GPIO type and how to read and write values.

```
-----  
; Enter the extended function mode, interruptible double-write  
-----  
Mov     dx, 2eh           ; Enter to extended function mode  
Mov     al, 87h  
Out     dx,al  
Out     dx,al  
-----  
; Configure logical device7 , configuration register CRF0,CRF1,CRF2  
; CRF0 (GP0-GP7 I/O selection register.)  
;     When set to a "1", respective GPIO port is programmed as input port.  
;     When set to a "0", respective GPIO port is programmed as an output port.  
; CRF1 (GP0-GP7 data register.)  
;     if a port is programmed to be an output port, then its respective bit can be  
;     read/written.  
;     If a port is programmed to be an input port, then its respective bit can only  
;     be read.  
; CRF2 (GP0-7 inversion register.)  
;     When set to a "1", the incoming/outgoing port value is inverted.  
;     When set to a "0", the incoming/outgoing port value is same as in data  
;     register.  
-----  
Mov     dx,2eh  
Mov     al,07h  
Out     dx,al           ; point to Logical Device Number Reg.  
Mov     dx,2fh  
Mov     al,07h  
Mov     dx,al           ; Select Logical devive 7  
  
Mov     dx,2eh  
Mov     al,f0h  
Out     dx,al           ;Select CRF0  
Mov     dx,2fh  
Mov     al,0fh           ;Set GP0-3 is input and GP4-7 is output  
Out     dx,al           ;update CRF0 with value 0Fh  
  
Mov     dx,2eh  
Mov     al,f1h  
Out     dx,al           ;Select CRF1  
Mov     dx,2fh  
In      al,dx  
And     al,0fh           ;read GP0-3 value  
Mov     ah,f0h           ;Set GP4-7 value is "1111xxxxb"  
Or      al,ah  
Out     dx,al           ; update CRF1 with value
```

```
;-----  
; Exit extended function mode  
;-----  
Mov    dx,2eh  
Mov    al,aah  
Out    dx,al
```

## Chapter 5 Technical Reference

This section outlines the errors that may occur when you operate the system, and also gives you the suggestions on solving the problems.

Topic include:

- Trouble Shooting for Post Beep & Error Messages
- Technical Reference

### Trouble Shooting for Post Beep and Error Messages

The following information informs the Post Beep & error messages. Please adjust your systems according to the messages below. Make sure all the components and connectors are in proper position and firmly attached. If the errors still exist, please contact with your distributor for maintenance.

#### POST BEEP

Currently there are two kinds of beep codes in BIOS setup.

- One indicates that a video error has occurred and the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by three short beeps.
- The other indicates that an error has occurred in your DRAM. This beep code consists of a constant single long beep.

#### CMOS BATTERY FAILURE

When the CMOS battery is out of work or has run out, the user has to replace it with a new battery.

### **CMOS CHECKSUM ERROR**

This error informs that the CMOS has corrupted. When the battery runs weak, this situation might happen. Please check the battery and change a new one when necessary.

### **DISK BOOT FAILURE**

When you can't find the boot device, insert a system disk into Drive A and press < Enter >. Make sure both the controller and cables are all in proper positions, and also make sure the disk is formatted. Then reboot the system.

### **DISKETTE DRIVES OR TYPES MISMATCH ERROR**

When the diskette drive type is different from CMOS, please run setup or configure the drive again.

### **ERROR ENCOUNTERED INITIALIZING HARD DRIVE**

When you can't initialize the hard drive, ensure the following things:

1. The adapter is installed correctly
2. All cables are correctly and firmly attached
3. The correct hard drive type is selected in BIOS Setup

### **ERROR INITIALIZING HARD DISK CONTROLLER**

When this error occurs, ensure the following things:

1. The cord is exactly installed in the bus.
2. The correct hard drive type is selected in BIOS Setup
3. Whether all of the jumpers are set correctly in the hard drive

### **FLOPPY DISK CONTROLLER ERROR OR NO CONTROLLER PRESENT**

When you cannot find or initialize the floppy drive controller, please ensure the controller is in proper BIOS Setup. If there is no floppy drive installed, ensure the Diskette Drive selection in Setup is set to NONE.

### **KEYBOARD ERROR OR NO KEYBOARD PRESENT**

When this situation happens, please check keyboard attachment and no keys being pressed during the boot. If you are purposely configuring the system without a keyboard, set the error halt condition in BIOS Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot procedure.

### **MEMORY ADDRESS ERROR**

When the memory address indicates error, you can use this location along with the memory map for your system to find and replace the bad memory chips.

### **MEMORY SIZE HAS CHANGED**

Memory has been added or removed since last boot. In EISA mode, use Configuration Utility to re-configure the memory configuration. In ISA mode enter BIOS Setup and enter the new memory size in the memory fields.

### **MEMORY VERIFYING ERROR**

It indicates an error verifying a value is already written to memory. Use the location along with your system's memory map to locate the bad chip.

### **OFFENDING ADDRESS MISSING**

This message is used in connection with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem cannot be isolated.

### **REBOOT ERROR**

When this error occurs that requires you to reboot. Press any key and the system will reboot.

### **SYSTEM HALTED**

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL.

## Technical Reference

### Physical and Environmental

Temperature: Operating 0°C ~ 50°C

Relative humidity 5 % to 95 % non-condensing

DC-AC adapter

Input AC Voltage Range: 100V~240V/1A, 50Hz ~60Hz

Output DC Voltage: 19V/3.42A Maximal

Surface Temperature of Chassis :

5°C to 45°C (W/HDD)/0°C to 50°C (W/CFast CF card only)

### Real-Time Clock and Non-Volatile RAM

The FX5636 contains a real-time clock compartment that maintains the date and time in addition to storing configuration information about the computer system. It contains 14 bytes of clock and control registers and 114 bytes of general purpose RAM. Because of the use of CMOS technology, it consumes very little power and can be maintained for long periods of time using an internal Lithium battery. The contents of each byte in the CMOS RAM are listed below:

Address	Description
00	Seconds
01	Second alarm
02	Minutes
03	Minute alarm
04	Hours
05	Hour alarm
06	Day of week
07	Date of month
08	Month
09	Year
0A	Status register A
0B	Status register B
0C	Status register C

Address	Description
0D	Status register D
0E	Diagnostic status byte
0F	Shutdown status byte
10	Diskette drive type byte, drive A and B
11	Fixed disk type byte, drive C
12	Fixed disk type byte, drive D
13	Reserved
14	Equipment byte
15	Low base memory byte
16	High base memory byte
17	Low expansion memory byte
18	High expansion memory byte
19-2D	Reserved
2E-2F	2-byte CMOS checksum
30	Low actual expansion memory byte
31	High actual expansion memory byte
32	Date century byte
33	Information flags (set during power on)
34-7F	Reserved for system BIOS

**CMOS RAM Map**

Register	Description
00h -10h	Standard AT-compatible RTC and Status and Status Register data definitions
11h – 13h	Varies
14h	Equipment Bits 7-6 Number of Floppy Drives 00 1 Drive 01 2 Drives Bits 5-4 Monitor Type 00 Not CGA or MDA 01 40x25 CGA 01 2 Drives 80x25 CGA Bits 3 Display Enabled 0 Disabled 1 Enabled Bit 2 Keyboard Enabled 00 Not CGA or MDA 01 40x25 CGA 01 2 Drives 80x25 CGA Bit 1 Math Coprocessor Installed 0 Absent 1 Present Bit 0 Floppy Drive Installed 0 Disabled 1 Enabled
15h	Base Memory (in 1KB increments), Low Byte
16h	Base Memory (in 1KB increments), High Byte
17h	IBM-compatible memory (in 1KB increments), Low Byte
18h	IBM-compatible memory (in 1KB increments), High Byte (max 15 MB)
19h-2Dh	Varies
2Eh	Standard CMOS RAM checksum, high byte
2Fh	Standard CMOS RAM checksum, low byte
30h	IBM-compatible Extended Memory, Low Byte (POST) in KB
31h	IBM-compatible Extended Memory, High Byte (POST) in KB
32h	Century Byte
33h	Reserved. Do not use
34h	Reserved. Do not use
35h	Low byte of extended memory (POST) in 64 KB
36h	High byte of extended memory (POST) in 64 KB
37h-3Dh	Varies
3Eh	Extended CMOS Checksum, Low Byte (including 34h-3Dh)
3Fh	Extended CMOS Checksum, High Byte (including 34h-3Dh)



### I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses, which also becomes the identity of the device. There is a total of 1K-port address space available. The following table lists the I/O port addresses used on the Industrial CPU Card.

Address	Device Description
000h - 01Fh	DMA Controller #1
020h - 03Fh	Interrupt Controller #1
040h - 05Fh	Timer
060h - 06Fh	Keyboard Controller
070h - 071h	Real Time Clock, NMI
080h - 09Fh	DMA Page Register
0A0h - 0BFh	Interrupt Controller #2
0C0h - 0DFh	DMA Controller #2
0F0h	Clear Math Coprocessor Busy Signal
0F1h	Reset Math Coprocessor
170h - 17Fh	IDE Controller
2E0h - 2EFh	Serial Port #6(COM6)
2E8h - 2EFh	Serial Port #4(COM4)
2F8h - 2FFh	Serial Port #2(COM2)
376h	IDE Controller
378h - 37Ah	LPT1
3B0h - 3DFh	VGA Controller
3E0h - 3EFh	Serial Port #5(COM5)
3E8h - 3EFh	Serial Port #3(COM3)
3F8h - 3FFh	Serial Port #1(COM1)
4D0h - 4D1h	System Board Resource
CF0h - CFFh	System Board Resource
C000h - C900h	System Board Resource
D800h - D8Feh	Ethernet Controller (LAN1)
E800h - E8Feh	Ethernet Controller (LAN2)

### Interrupt Request Lines (IRQ)

There are a total of 15 IRQ lines available on the Industrial CPU Card. Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on the Industrial CPU Card.

Level	Function
IRQ0	System Timer Output
IRQ1	Keyboard
IRQ2	Interrupt Cascade
IRQ3	Serial Port #2
IRQ4	Serial Port #1
IRQ5	USB
IRQ6	USB
IRQ7	LPT
IRQ8	Real Time Clock
IRQ9	-
IRQ10	Serial Port #4
IRQ11	Serial Port #3
IRQ12	USB#3
IRQ13	FPU
IRQ14	IDE
IRQ15	IDE Controller

### DMA Channel Map

The equivalents of two 8237A DMA controllers are implemented in the FX5636 board. Each controller is a four-channel DMA device that will generate the memory addresses and control signals necessary to transfer information directly between a peripheral device and memory. This allows high speed information transfer with less CPU intervention. The two DMA controllers are internally cascaded to provide four DMA channels for transfers to 8-bit peripherals (DMA1) and three channels for transfers to 16-bit peripherals (DMA2). DMA2 channel 0 provides the cascade interconnection between the two DMA devices, thereby maintaining IBM PC/AT compatibility.

The following is the system information of DMA channels:

DMA Controller 1	DMA Controller 2
Channel 0: Spare	Channel 4: Cascade for controller 1
Channel 1: Reserved for IBM SDLC	Channel 5: Spare
Channel 2: Diskette adapter	Channel 6: Spare
Channel 3: Spare	Channel 7: Spare

## Serial Ports

The ACEs (Asynchronous Communication Elements ACE1 to ACE2) are used to convert parallel data to a serial format on the transmit side and convert serial data to parallel on the receiver side. The serial format, in order of transmission and reception, is a start bit, followed by five to eight data bits, a parity bit (if programmed) and one, one and half (five-bit format only) or two stop bits. The ACEs are capable of handling divisors of 1 to 65535, and produce a 16x clock for driving the internal transmitter logic.

Provisions are also included to use this 16x clock to drive the receiver logic. That's Also included in the ACE a completed MODEM control capability, and a processor interrupt system that may be software tailored to the computing time required to handle the communications link.

The following table is a summary of each ACE accessible register

DLAB	Port Address	Register
0	Base + 0	Receiver buffer (read)
		Transmitter holding register (write)
0	Base + 1	Interrupt enable
X	Base + 2	Interrupt identification (read only)
X	Base + 3	Line control
X	Base + 4	MODEM control
X	Base + 5	Line status
X	Base + 6	MODEM status
X	Base + 7	Scratched register
1	Base + 0	Divisor latch (least significant byte)
1	Base + 1	Divisor latch (most significant byte)

### Receiver Buffer Register (RBR)

Bit 0-7: Received data byte (Read Only)

### Transmitter Holding Register (THR)

Bit 0-7: Transmitter holding data byte (Write Only)

### Interrupt Enable Register (IER)

Bit 0: Enable Received Data Available Interrupt (ERBFI)

Bit 1: Enable Transmitter Holding Empty Interrupt (ETBEI)

Bit 2: Enable Receiver Line Status Interrupt (ELSI)

Bit 3: Enable MODEM Status Interrupt (EDSSI)

Bit 4: Must be 0

Bit 5: Must be 0

Bit 6: Must be 0

Bit 7: Must be 0

**Interrupt Identification Register (IIR)**

Bit 0: "0" if Interrupt Pending

Bit 1: Interrupt ID Bit 0

Bit 2: Interrupt ID Bit 1

Bit 3: Must be 0

Bit 4: Must be 0

Bit 5: Must be 0

Bit 6: Must be 0

Bit 7: Must be 0

**Line Control Register (LCR)**

Bit 0: Word Length Select Bit 0 (WLS0)

Bit 1: Word Length Select Bit 1 (WLS1)

WLS1	WLS0	Word Length
0	0	5 Bits
0	1	6 Bits
1	0	7 Bits
1	1	8 Bits

Bit 2: Number of Stop Bit (STB)

Bit 3: Parity Enable (PEN)

Bit 4: Even Parity Select (EPS)

Bit 5: Stick Parity

Bit 6: Set Break

Bit 7: Divisor Latch Access Bit (DLAB)

**MODEM Control Register (MCR)**

Bit 0: Data Terminal Ready (DTR)

Bit 1: Request to Send (RTS)

Bit 2: Out 1 (OUT 1)

Bit 3: Out 2 (OUT 2)

Bit 4: Loop

Bit 5: Must be 0

Bit 6: Must be 0

Bit 7: Must be 0

**Line Status Register (LSR)**

Bit 0: Data Ready (DR)

Bit 1: Overrun Error (OR)

Bit 2: Parity Error (PE)

Bit 3: Framing Error (FE)

Bit 4: Break Interrupt (BI)

Bit 5: Transmitter Holding Register Empty (THRE)

Bit 6: Transmitter Shift Register Empty (TSRE)

Bit 7: Must be 0

**MODEM Status Register (MSR)**

Bit 0: Delta Clear to Send (DCTS)

Bit 1: Delta Data Set Ready (DDSR)

Bit 2: Training Edge Ring Indicator (TERI)

Bit 3: Delta Receive Line Signal Detect (DSLSD)

Bit 4: Clear to Send (CTS)

Bit 5: Data Set Ready (DSR)

Bit 6: Ring Indicator (RI)

Bit 7: Received Line Signal Detect (RSLD)

**Divisor Latch (LS, MS)**

	LS	MS
Bit 0:	Bit 0	Bit 8
Bit 1:	Bit 1	Bit 9
Bit 2:	Bit 2	Bit 10
Bit 3:	Bit 3	Bit 11
Bit 4:	Bit 4	Bit 12
Bit 5:	Bit 5	Bit 13
Bit 6:	Bit 6	Bit 14
Bit 7:	Bit 7	Bit 15

Desired Baud Rate	Divisor Used to Generate 16x Clock
300	384
600	192
1200	96
1800	64
2400	48
3600	32
4800	24
9600	12
14400	8
19200	6
28800	4
38400	3
57600	2
115200	1

## Parallel Ports

### Register Address

Port Address	Read/Write	Register
Base + 0	Write	Output data
Base + 0	Read	Input data
Base + 1	Read	Printer status buffer
Base + 2	Write	Printer control latch

### Printer Interface Logic

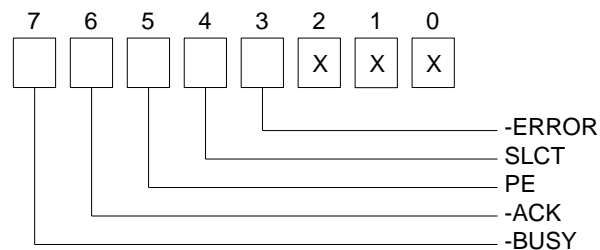
The parallel portion of the Winbond 83627 makes the attachment of various devices that accept eight bits of parallel data at standard TTL level.

### Data Swapper

The system microprocessor can read the contents of the printer's Data Latch through the Data Swapper by reading the Data Swapper address

### Printer Status Buffer

The system microprocessor can read the printer status by reading the address of the Printer Status Buffer. The bit definitions are described below:



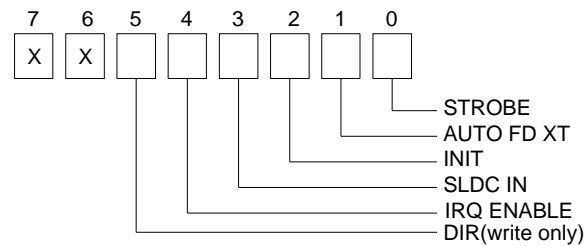
NOTE: X represents not used.



- Bit 7: This signal may become active during data entry, when the printer is off-line during printing, or when the print head is changing position or in an error state. When Bit 7 is active, the printer is busy and cannot accept data.
- Bit 6: This bit represents the current state of the printer's ACK signal. A 0 means the printer has received the character and is ready to accept another. Normally, this signal will be active for approximately 5 microseconds before receiving a BUSY message stops.
- Bit 5: A 1 means the printer has detected the end of the paper.
- Bit 4: A 1 means the printer is selected.
- Bit 3: A 0 means the printer has encountered an error condition.

### Printer Control Latch & Printer Control Swapper

The system microprocessor can read the contents of the printer control latch by reading the address of printer control swapper. Bit definitions are as follows:



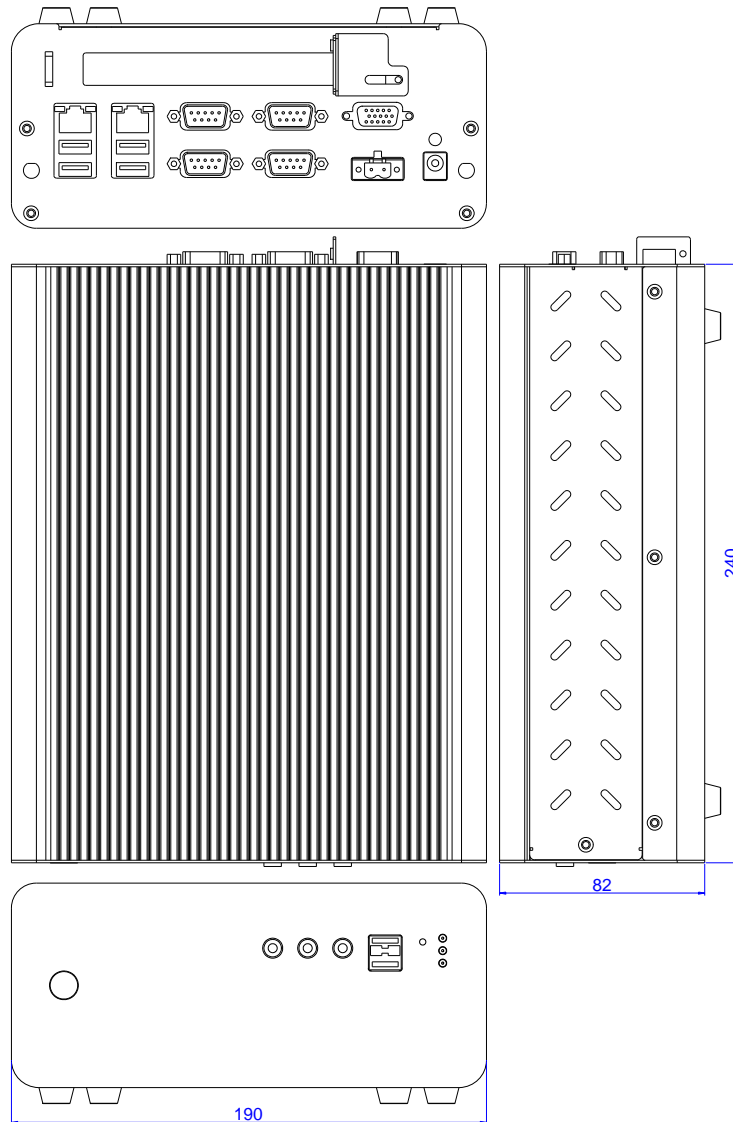
NOTE: X represents not used.

- Bit 5: Direction control bit. When logic 1, the output buffers in the parallel port are disabled allowing data driven from external sources to be read; when logic 0 they work as a printer port. This bit is write-only.
  
- Bit 4: A 1 in this position allows an interrupt to occur when ACK changes from low state to high state.
  
- Bit 3: A 1 in this bit position selects the printer.
  
- Bit 2: A 0 starts the printer (50 microseconds pulse, minimum).
  
- Bit 1: A 1 causes the printer to line-feed after a line is printed.
  
- Bit 0: A 0.5 microsecond minimum highly active pulse clocks data into the printer. Valid data must be present for a minimum of 0.5 microseconds before and after the strobe pulse.

## Appendix

### Dimension

a. FX5636



b. FX5636k1 universal fixers

